

Layerscape LX2160A Reference Design Board

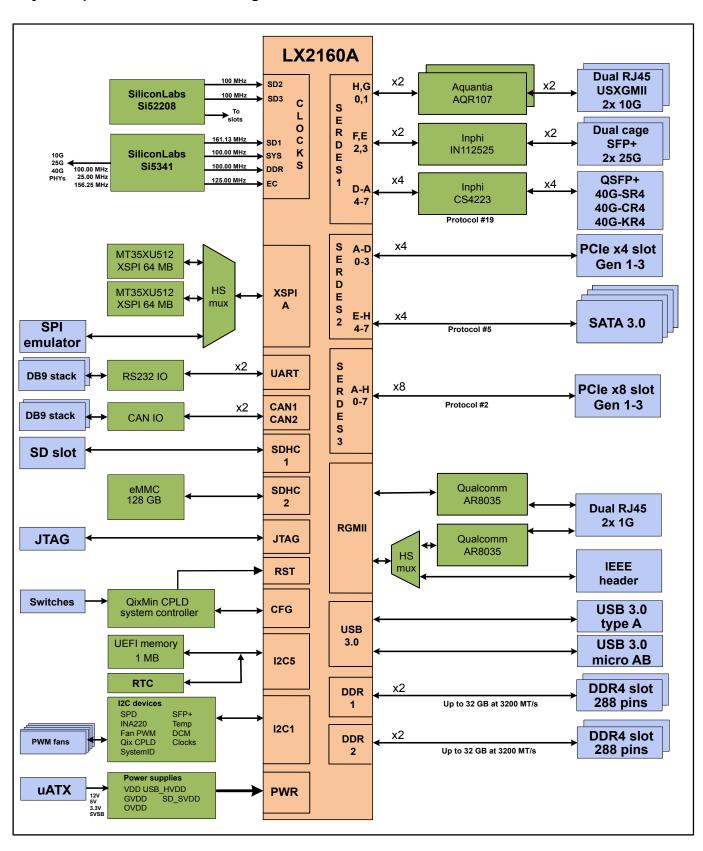
LX2160A-RDB

Last Updated: Nov 22, 2023

The LX2160A reference design board provides a comprehensive platform that enables design and evaluation of the LX2160A or LX2162A processors. It comes preloaded with a board support package (BSP) based on a standard Linux kernel.

- Enables network intelligence with the next generation Datapath (DPPA2) which provides differentiated offload and a rich set of IO, including 10 GE, 25 GE, 40 GE, and PCIe Gen3
- Delivers unprecedented efficiency and new virtualized networks
- Supports designs in 5G packet processing, network function virtualization, storage controller, white box switching, network interface cards, and mobile edge computing
- Supports all six LX2 family members (16-core LX2160A and LX2161A; 12-core LX2120A and LX2122A; and 8-core LX2080A and LX2082A)
- Certified to the Arm SystemReady ES standard to ensure interoperability with standard operating systems and hypervisors.

Layerscape LX2160A Block Diagram



Note: The information on this document is subject to change without notice.
WWW.nxp.com NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.