

An economical approach to product development -

M68HC12 Evaluation Board

The M68HC12A4EVB Evaluation Board (EVB) is an economical tool for designing and debugging code for, and evaluating the operation of, the MC68HC812A4 MCU. By providing the essential MCU timing and I/O circuitry, the EVB simplifies user evaluation of prototype hardware and software.

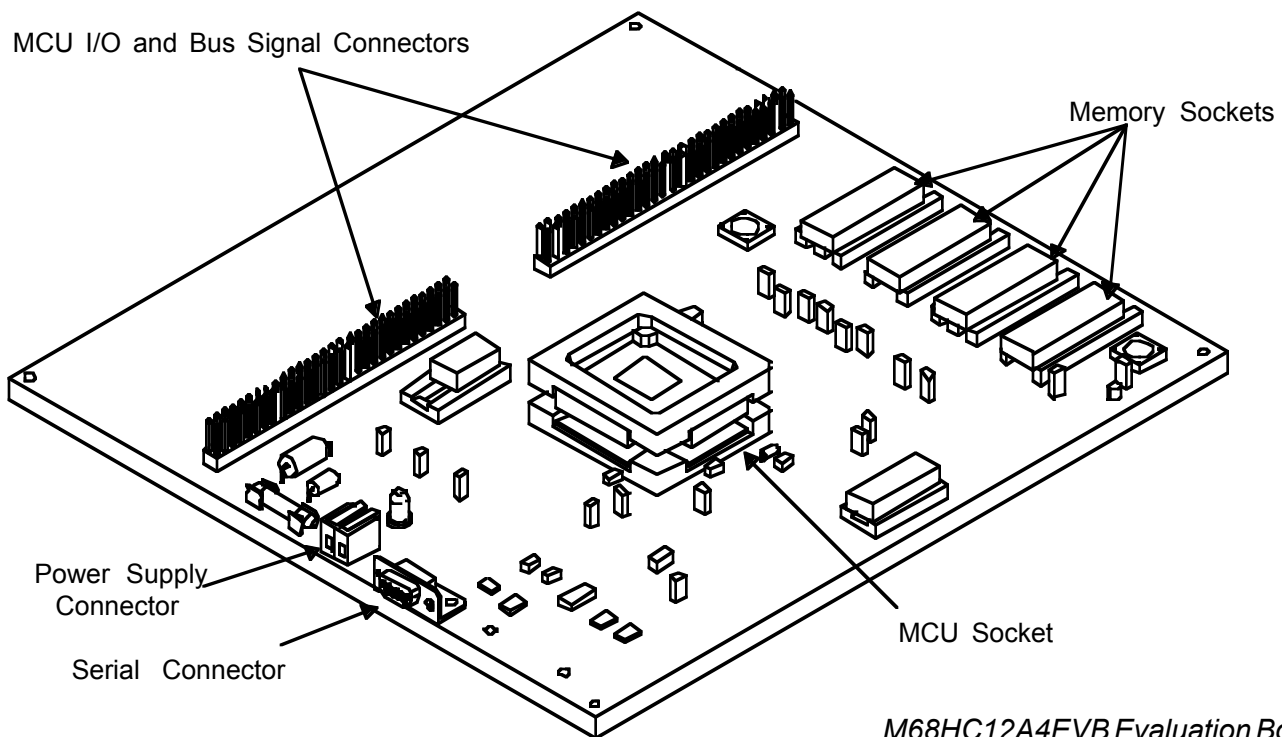
The EVB features a prototype area, which allows custom interfacing with the MCU's I/O and bus lines. These connections are broken out via on-board headers, which are immediately adjacent to the prototype area.

Optionally, the EVB can accommodate various types and configurations of external memory to suit a particular application's requirements. These custom configurations are effected by installing the appropriate memory chips in the EVB's memory sockets and by setting jumpers on the EVB to correctly establish the MC68HC812A4's memory-access operations.

The figure below shows the EVB's layout and locations of the major components, as viewed from the component side of the board..

Features

- **+3 to +5 Vdc operation**
- **RS-232C interfaces**
- **64K of on-board EPROM with resident D-Bug12 monitor/debugger program**
- **16K of on-board SRAM**
- **Jumper-selectable support for multiple memory device types and sizes**
- **Header connectors for access to the MCU's I/O and bus lines**
- **On board prototype area for customized interfacing with the MCU**
- **Low voltage inhibit protection**
- **Background mode operation**
- **Full support for either dumb-terminal or host-computer terminal interface**
- **MCUasm assembly language development toolkit**
- **File-transfer capability from a host computer, allowing off-board code generation**



M68HC12A4EVB Evaluation Board

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EVB OVERVIEW

The EVB is composed of three components:

1. Hardware – the board consists of an 8-inch by 8-inch multi-layer printed circuit board that provides the platform for interface and power connections to the MC68HC812A4 MCU chip, which is installed in a production socket.
2. Firmware – D-Bug12, the EVB's firmware-resident monitor program, provides a self-contained operating environment that allows writing, evaluation, and debugging of user programs.

The hardware is factory-configured to execute D-Bug12 without further configuration by the user. It is ready for use with an RS-232C terminal for writing and debugging user code.

D-Bug12 allows programming of the MC68HC812A4's on-chip EEPROM through commands that directly alter memory.

Commands are typed on the terminal's D-Bug12 prompt line and executed when the carriage-return (ENTER) key is pressed. D-Bug12 then displays either the appropriate response to the command or an error indication.

3. Software – the MCUasm assembly language toolset provides an integrated development environment which includes a project manager (MCUproject), a relocatable macro-assembler (MASM), a linker (MLINK), a librarian (AR), a Motorola S-Record generator (HEX) and a variety of other tools.

There are two methods to generate EVB user code:

1. For small programs or subroutines, D-Bug12's single-line assembler/disassembler may be used to place object code directly into the EVB's memory.
2. For larger programs, the Motorola MCUasm assembler may be used on a host computer to generate S-Record object files, which can then be loaded into the EVB's memory using D-Bug12's LOAD command.

If the M68HC812A4's background debug mode (BDM) interface serves as the user interface, both of the SCI ports become available for user applications. This mode requires a background debug development tool, such as Motorola's SDI™ Interface, and a host computer with the appropriate interface software. Note: D-Bug12 does not use the BDM interface.

The EVB can begin operation in either of two jumper-selectable modes at reset:

1. In normal mode, D-Bug12 immediately issues its command prompt on the terminal display and waits for a user entry.
2. In the alternate mode, execution begins directly with the user code in on-chip EEPROM. This hardware function is also available for customized use in the prototype area.

SPECIFICATIONS SUMMARY

The tables below summarize EVB specifications and minimum host PC requirements.

EVB Specifications

Characteristic	Specification	Characteristic	Specification
MCU	MC68HC812A4	Communications ports	Two RS-232C DCE ports
MCU I/O ports	HCMOS compatible	Board dimensions	8 x 8 in.
EPROM: Wide mode Narrow mode	64, 128, 256, 512, or 1024 KB 32, 64, 128, 256, or 512 KB	SRAM: Wide mode Narrow mode	16, 64, 256, or 1024 KB 8, 32, 128, or 512 KB
EEPROM: Wide mode Narrow mode	64, 128, 256, or 512 KB 32, 64, 128, or 256 KB		

Minimum Requirements

Characteristic	Specification
Terminal	RS-232C compatible terminal
Host PC (optional)	386-based PC or higher with an RS-232C serial port. Requires a user-supplied communications program capable of emulating a dumb terminal.
Input/Output	Serial communication port
Cable	RS-232C compatible
External Power Supply	+3.5 to +5.0 Vdc @ 150 mA (max.), fuse-protected @ 1.5 A

ORDERING INFORMATION

EVB Part Number

Evaluated MCU	EVB Part Number	Software
MC68HC812A4	M68HC12A4EVB	MCUasm (included with the EVB)

WARRANTY INFORMATION

Motorola provides a one year limited warranty.

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