

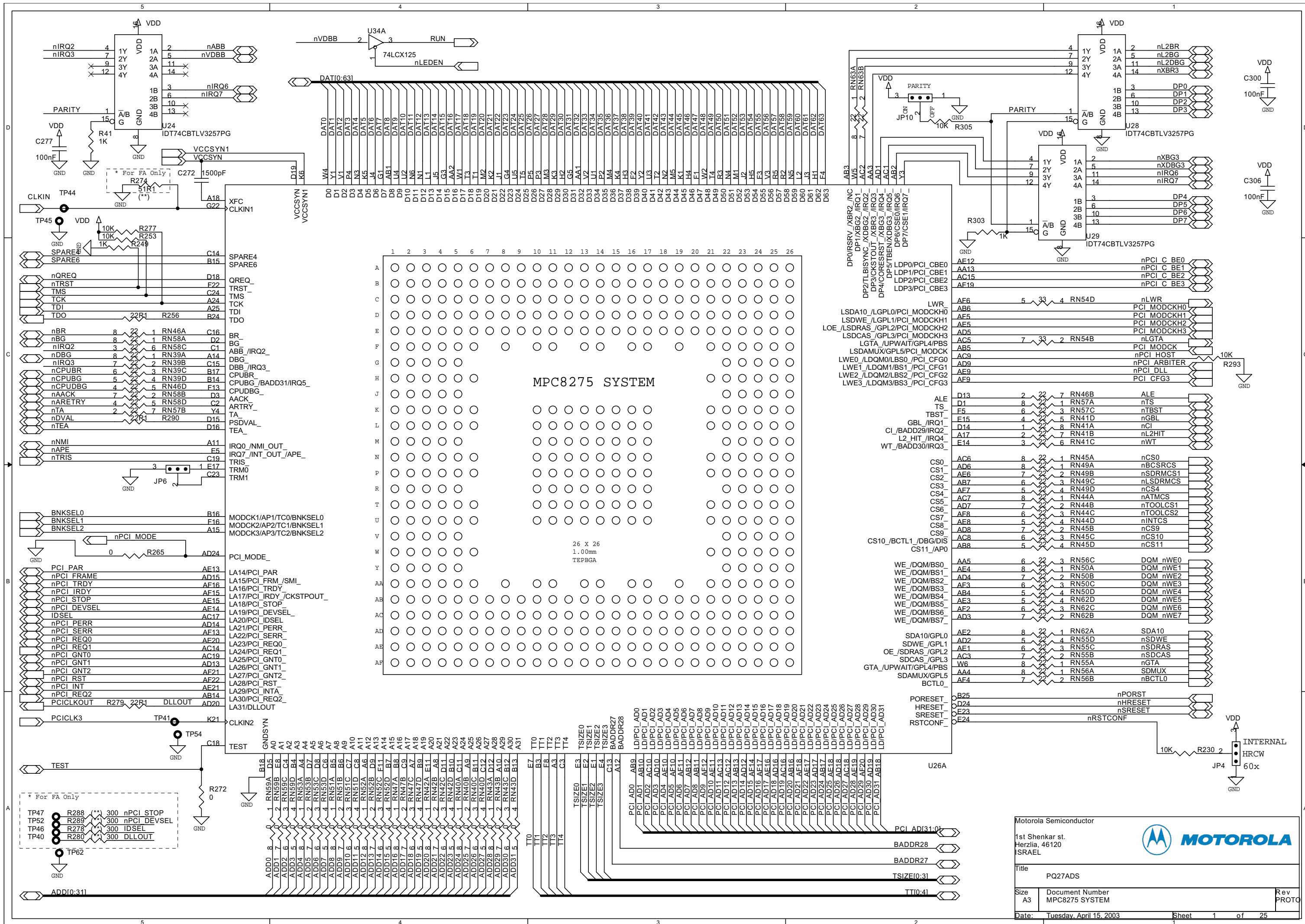
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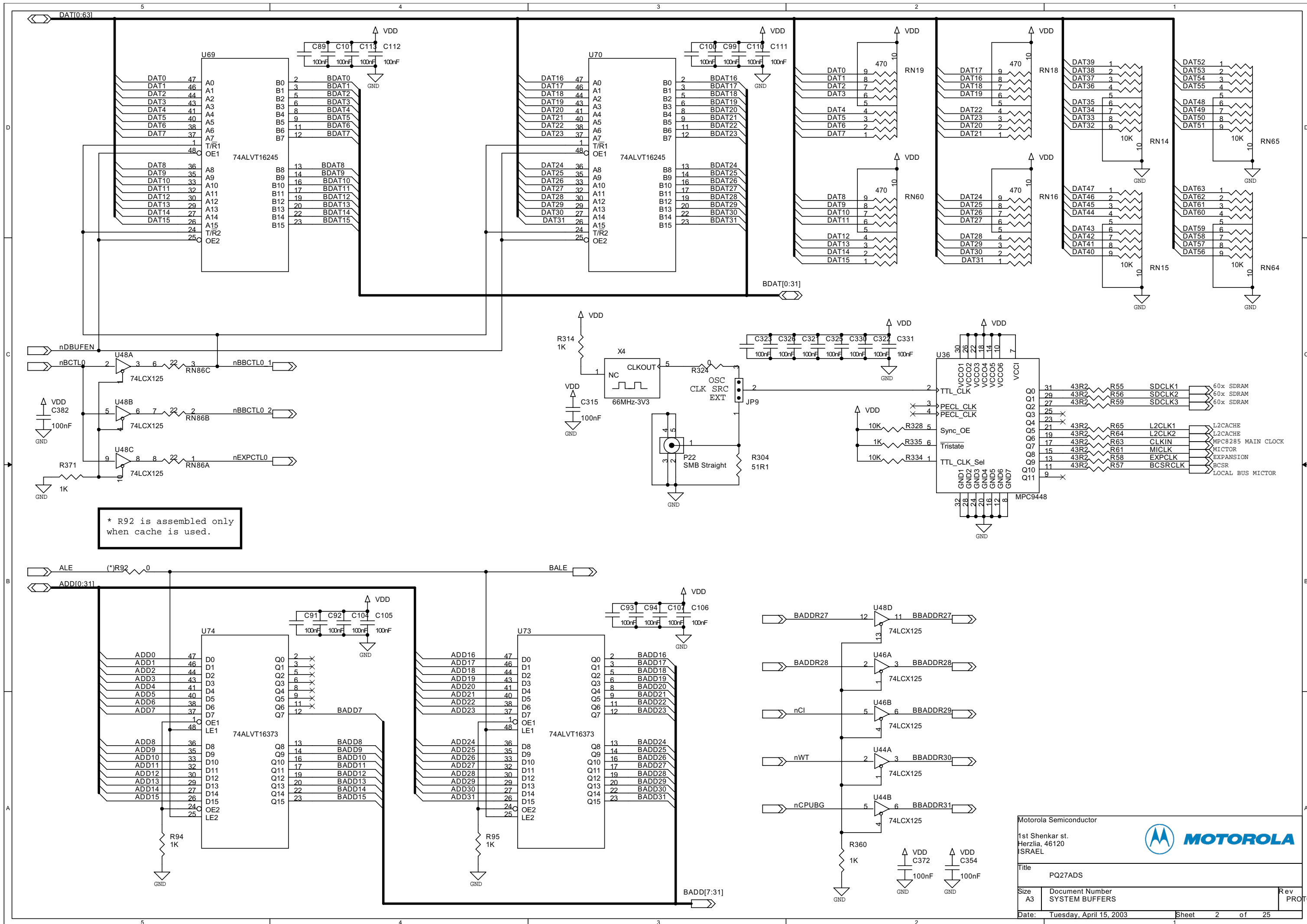
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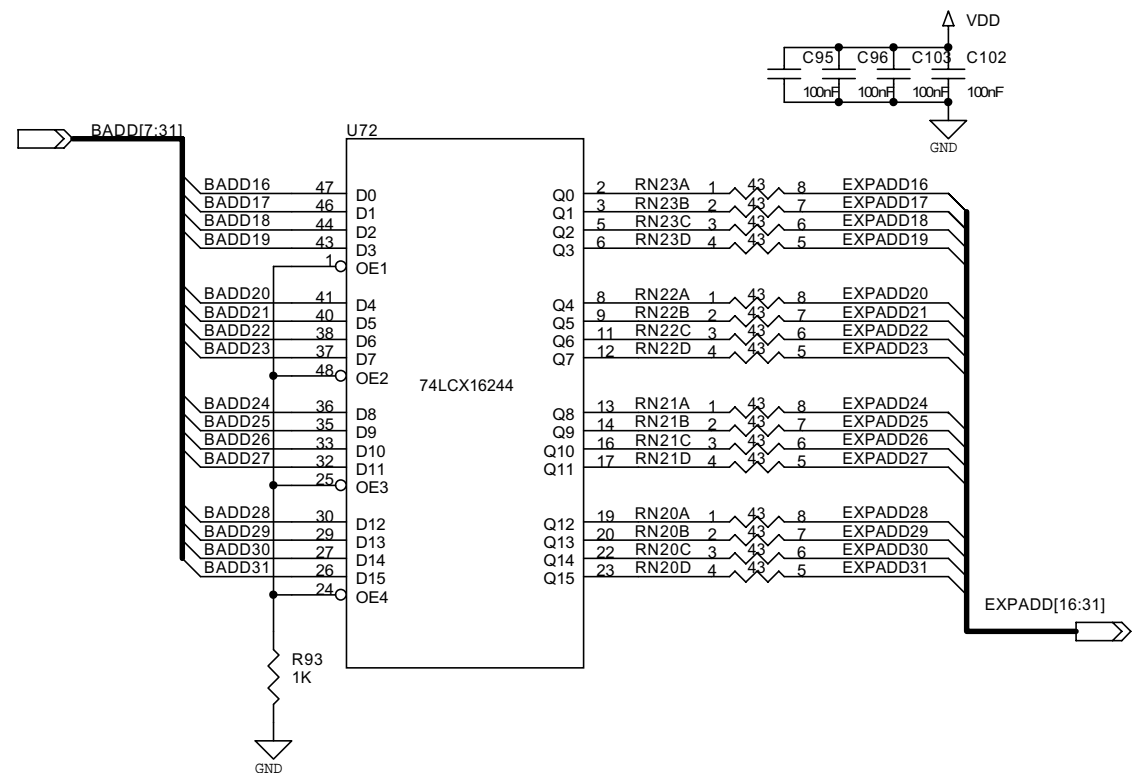
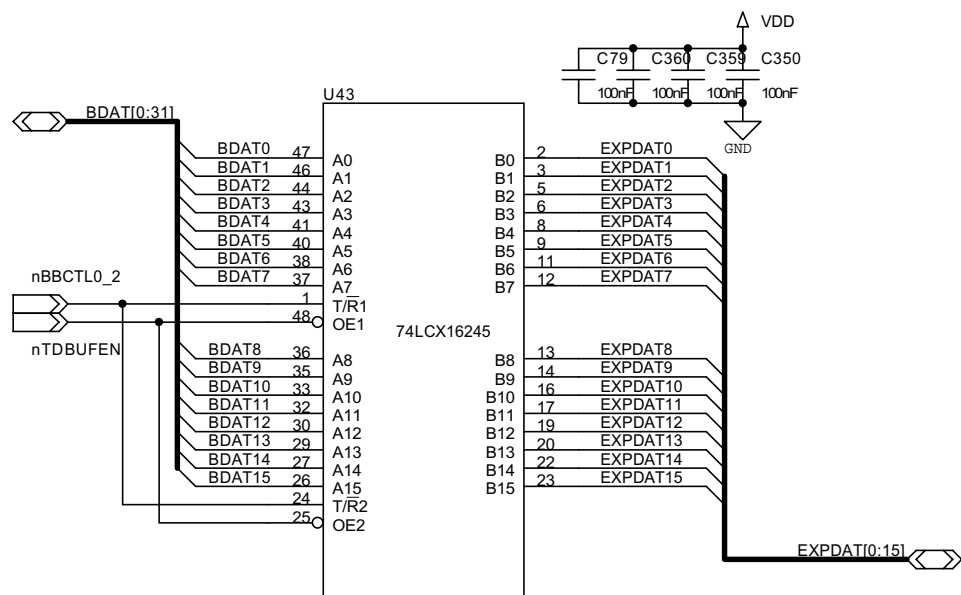
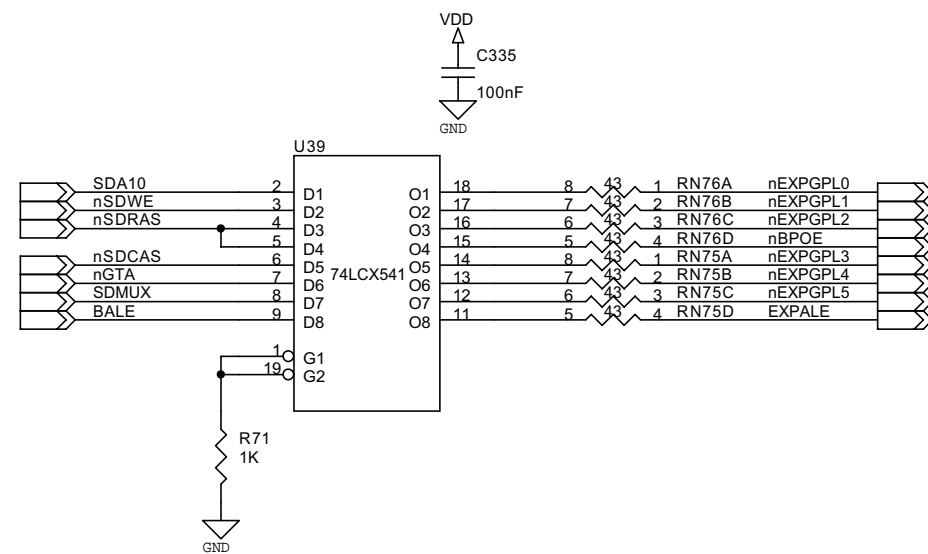
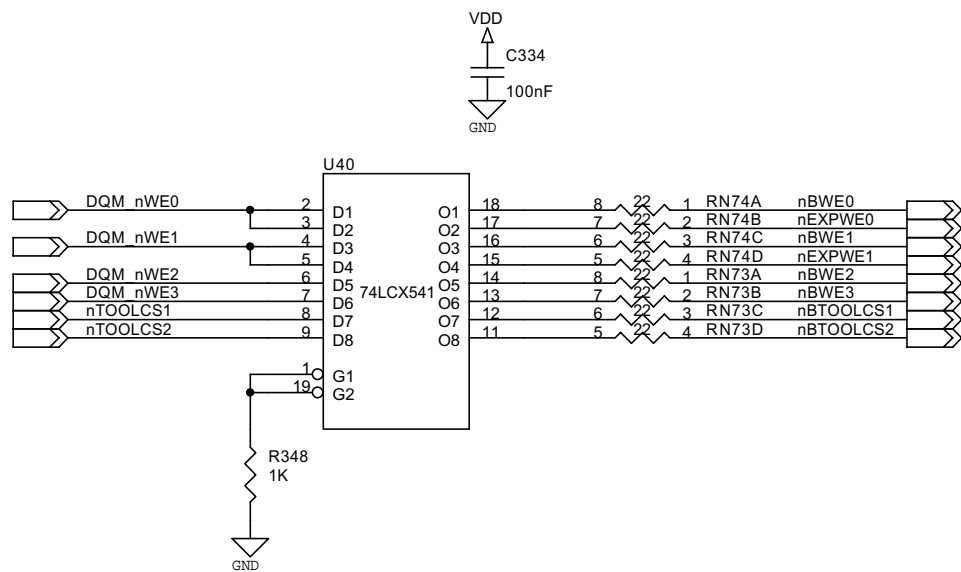


PQ2FADS-VR

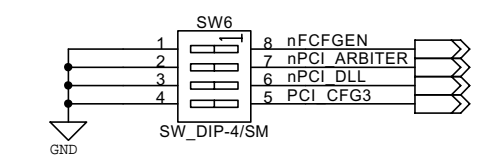
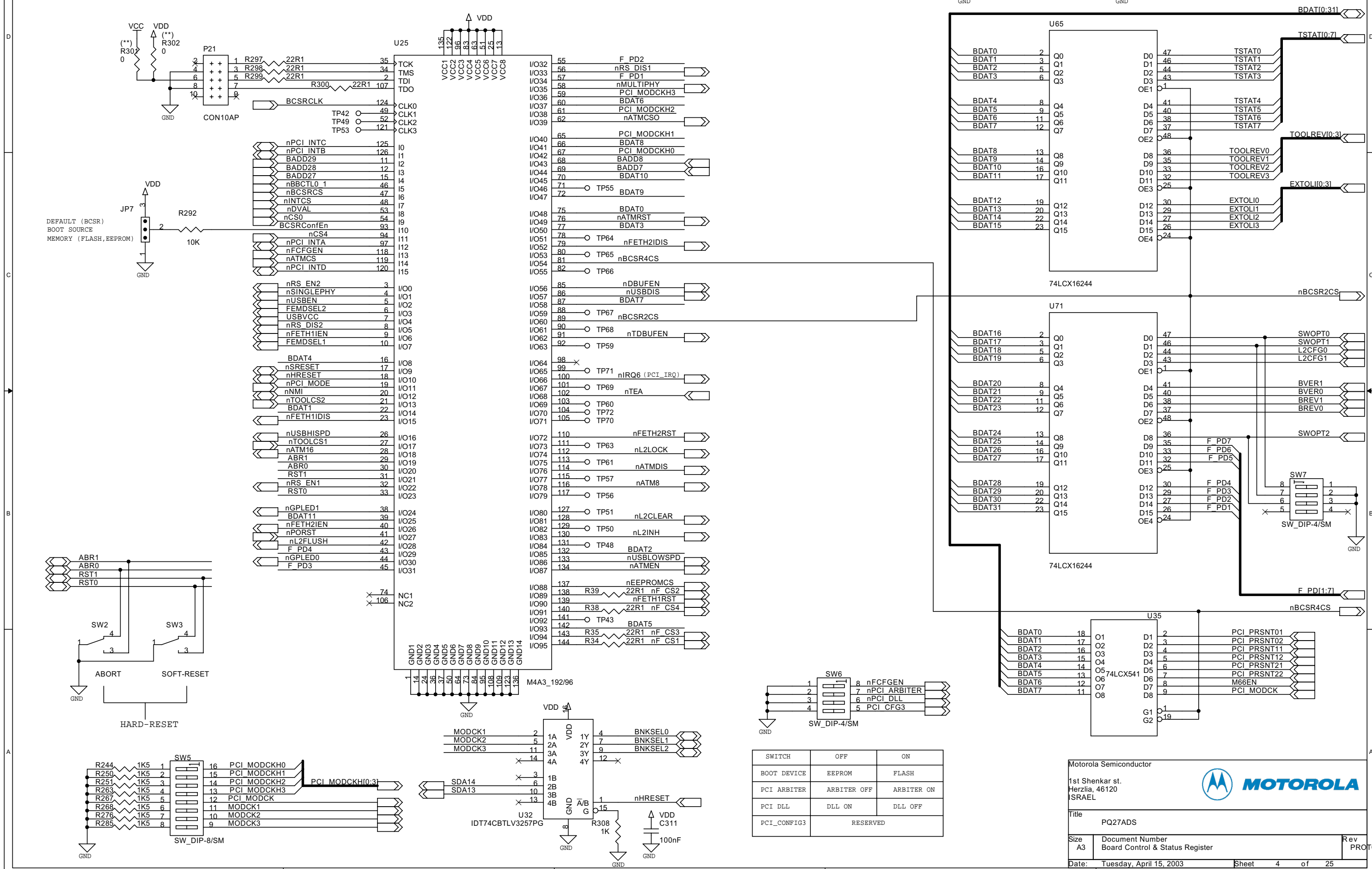
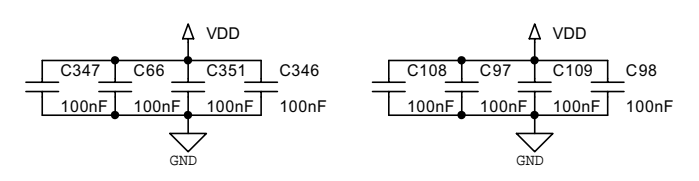
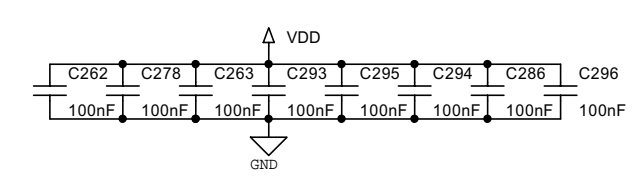
MPC8275 MotherBoard







* R301 or R302 is assembled depending on the ISP programmer - R302 assembled.



SWITCH	OFF	ON
BOOT DEVICE	EEPROM	FLASH
PCI ARBITER	ARBITER OFF	ARBITER ON
PCI DLL	DLL ON	DLL OFF
PCI_CONFIG3	RESERVED	

Motorola Semiconductor

1st Shenkar st.
Herzlia, 46120
ISRAEL

Title

PQ27ADS

Size

A3

Document Number

Board Control & Status Register

Rev

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Date:

Tuesday, April 15, 2003

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of

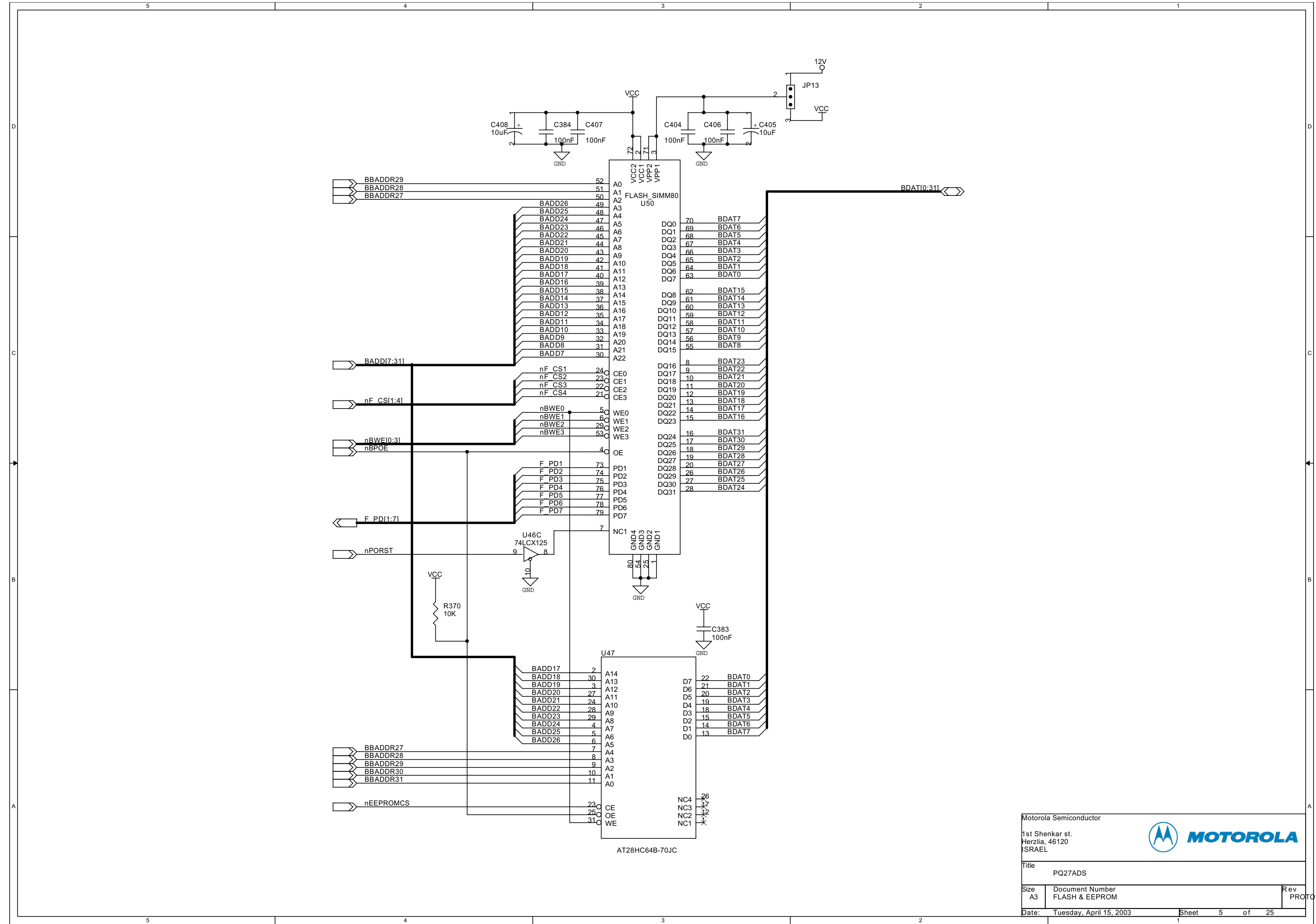
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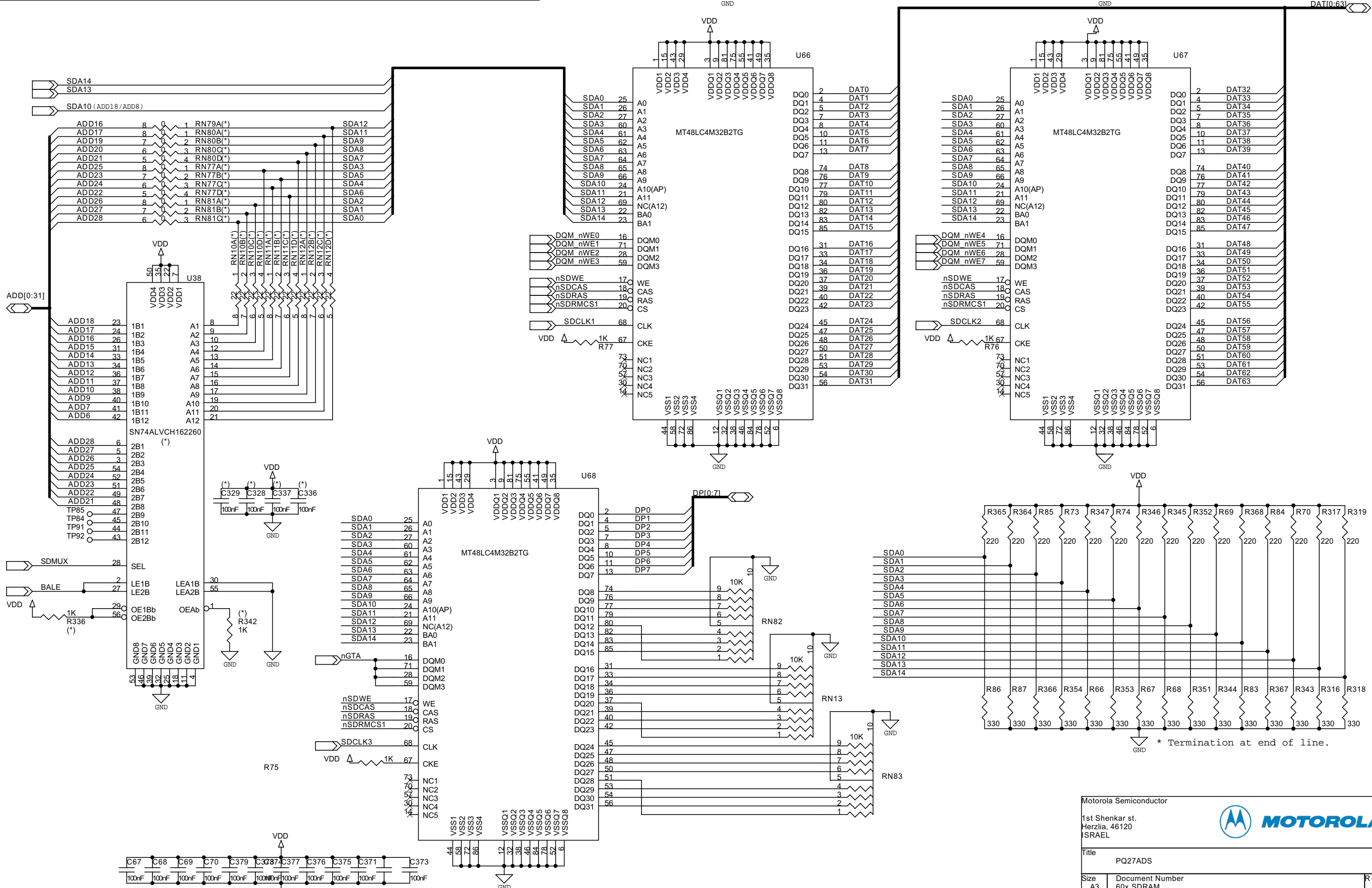
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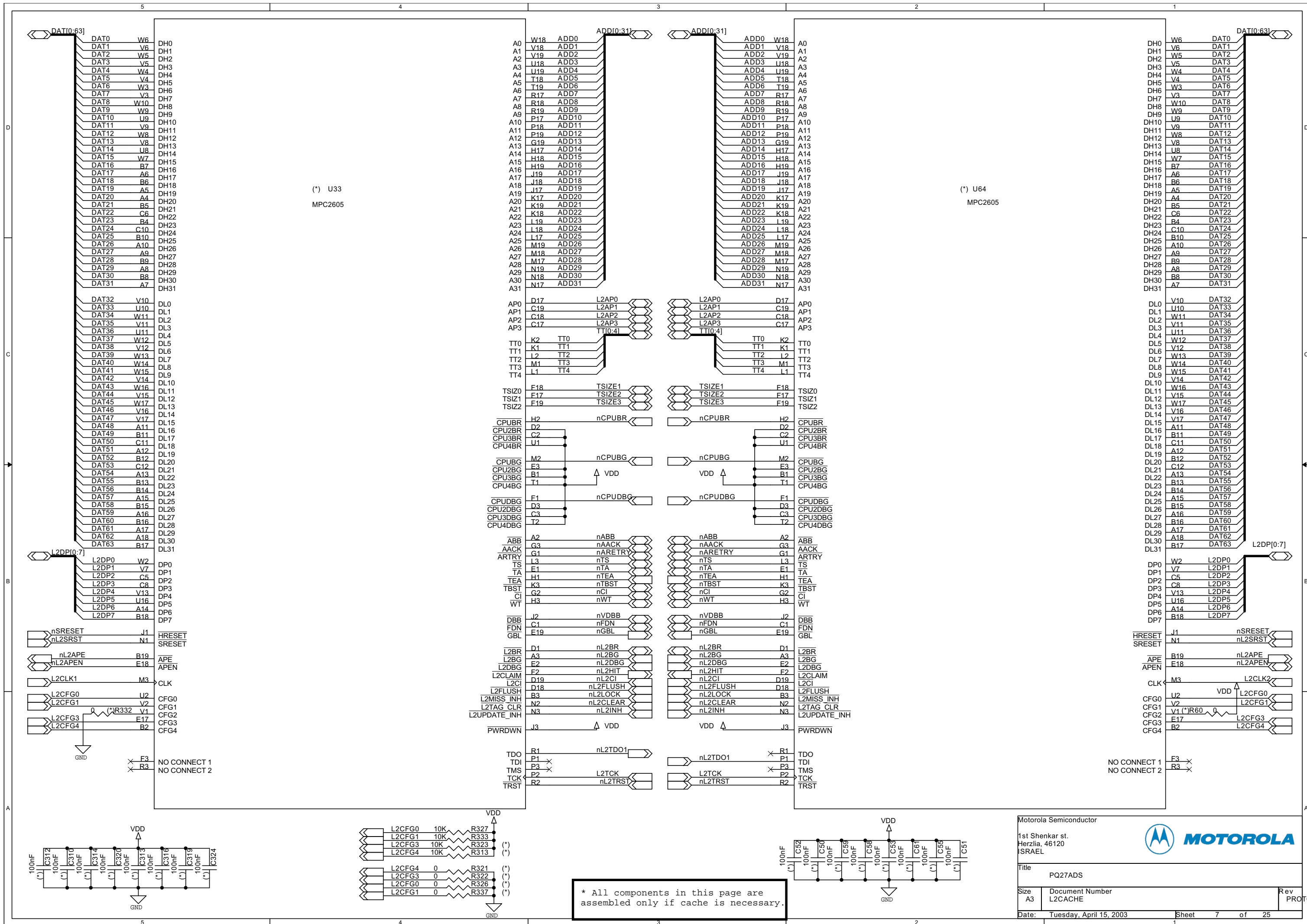
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A



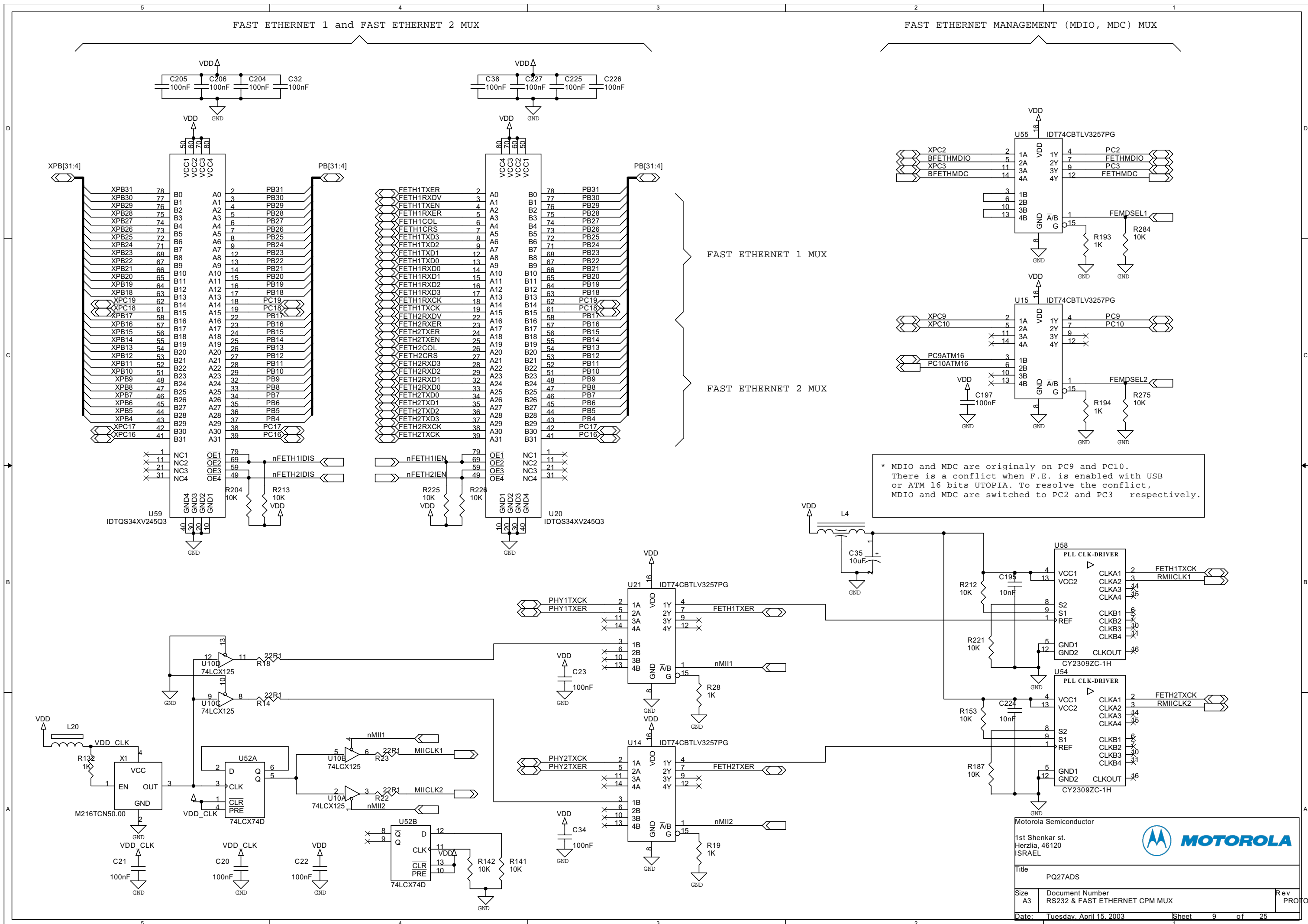
* RN77, RN79, RN80, RN81 are assembled if cache is not used - factory assembled.
* C328, C329, C336, C337, U43, RN10, RN11, RN12, R319, R320 are assembled if cache is used - not assembled.

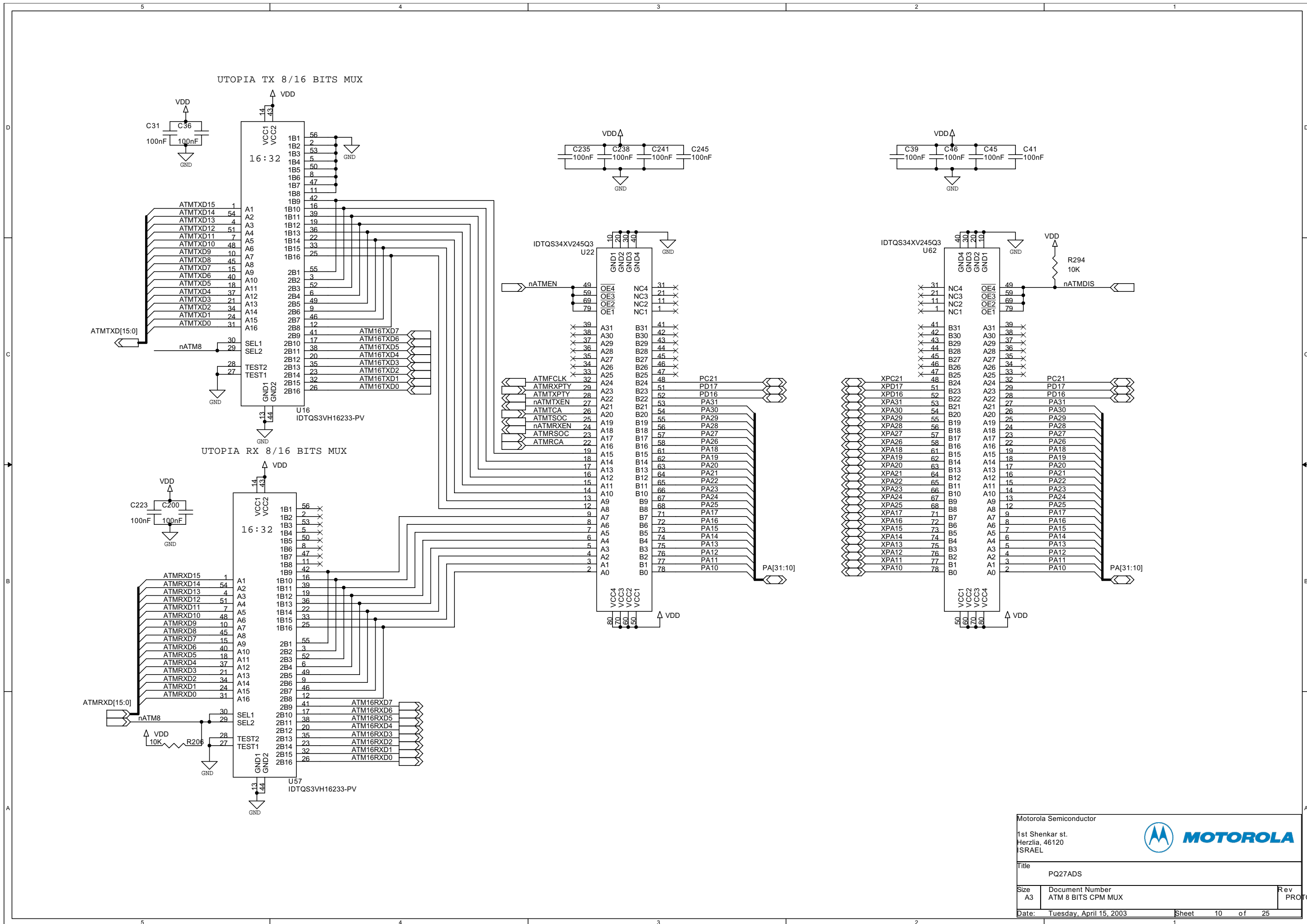




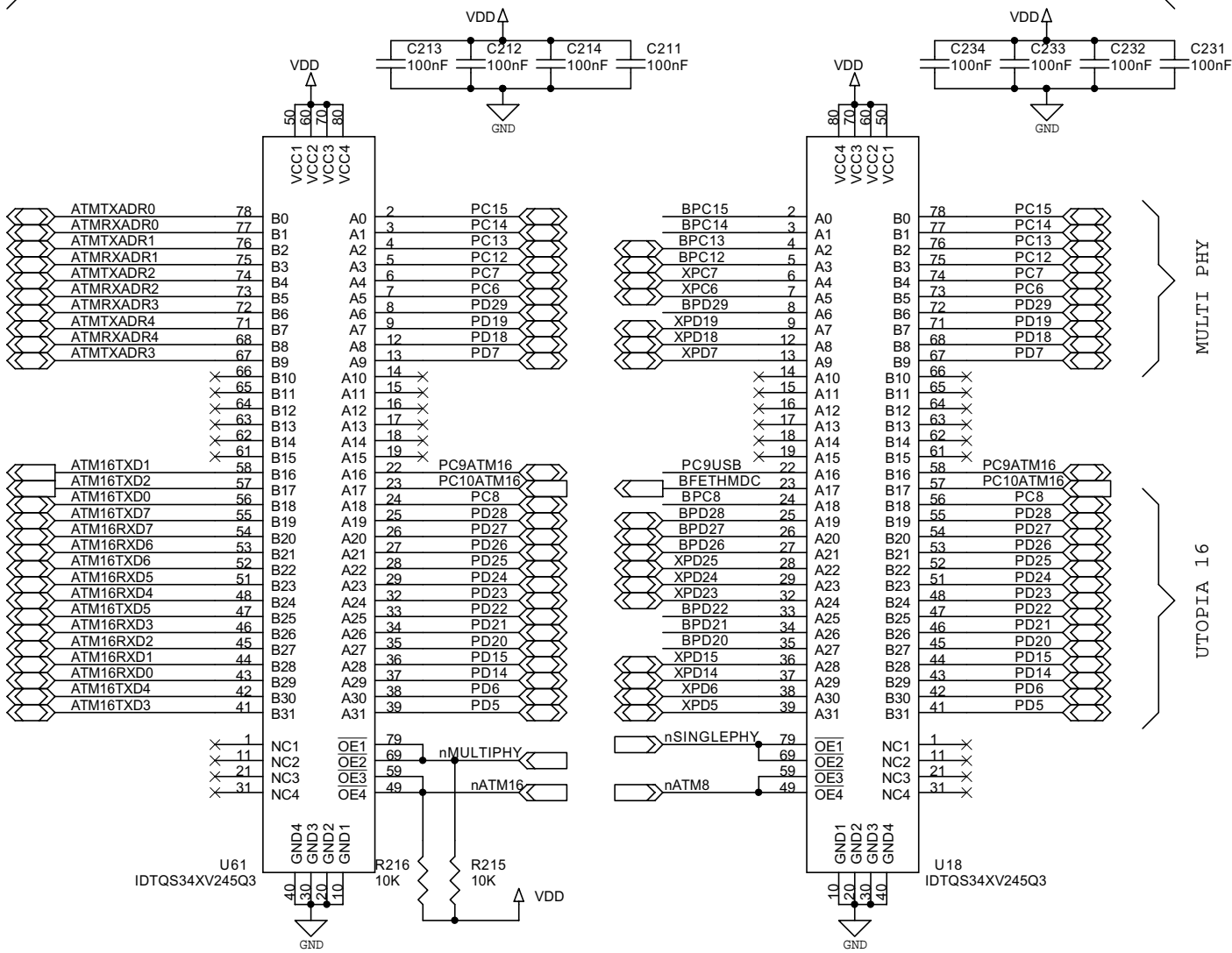
* All components in this page are assembled only if cache is necessary.



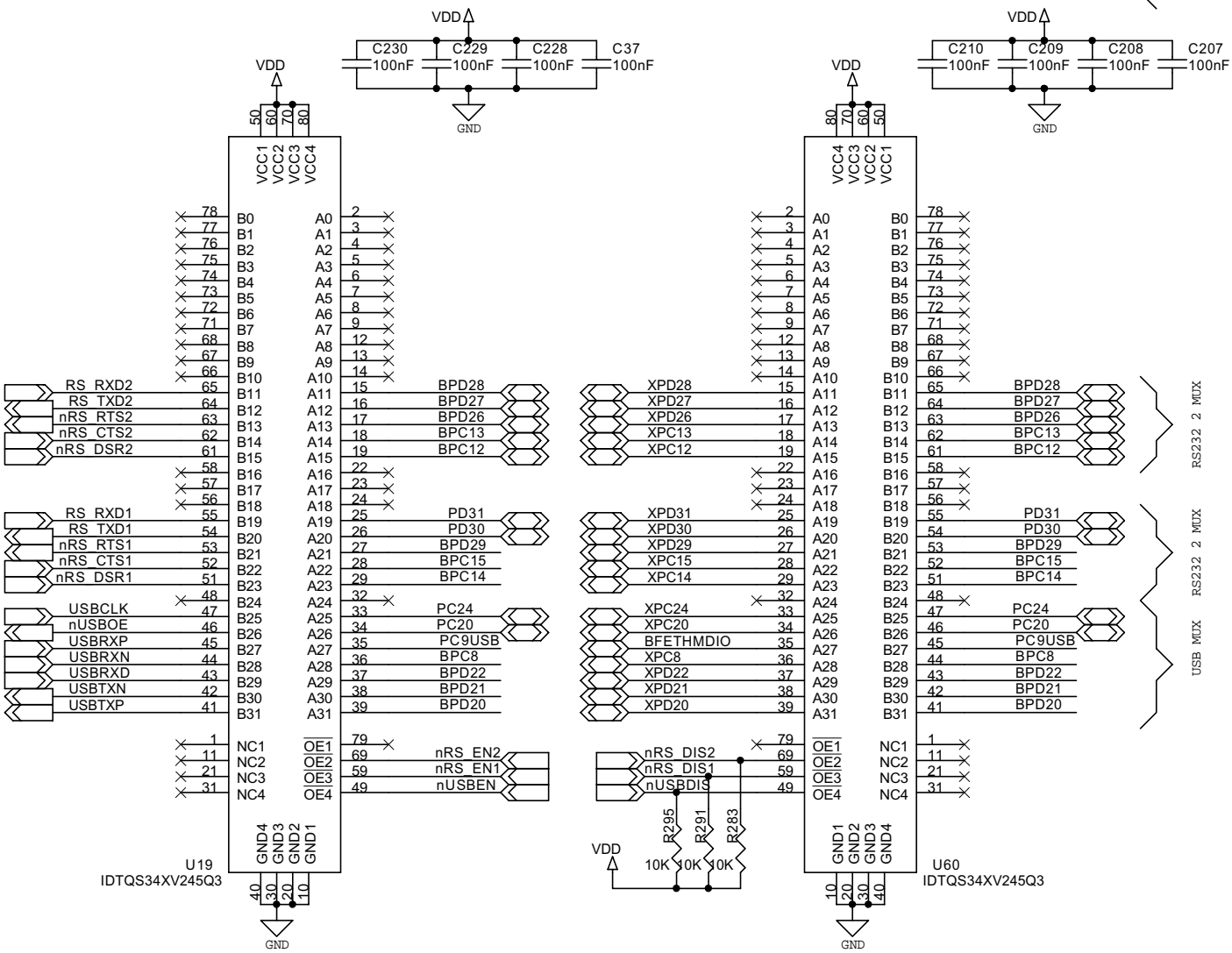


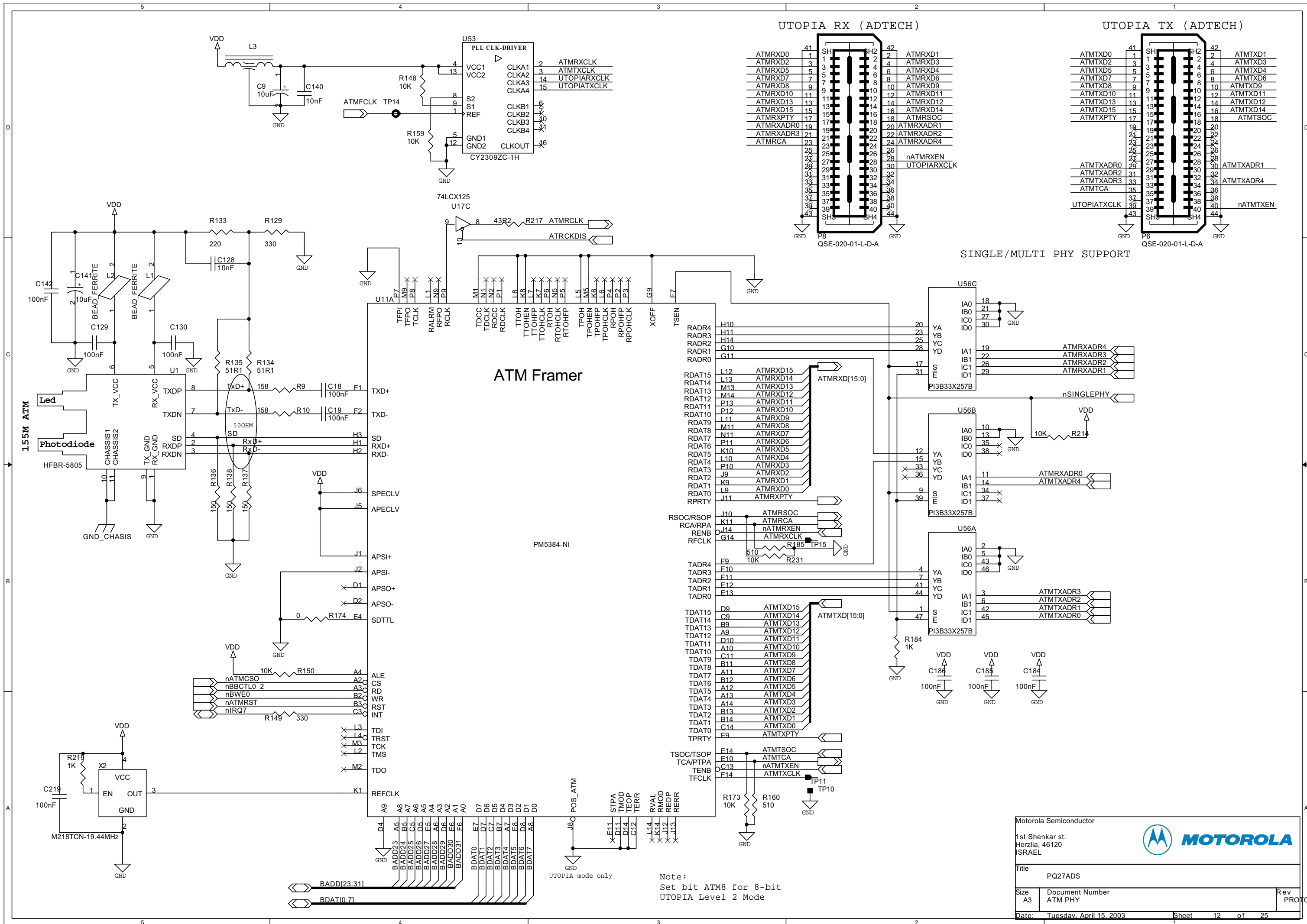


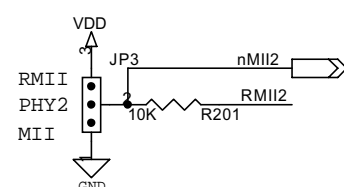
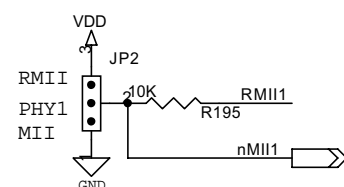
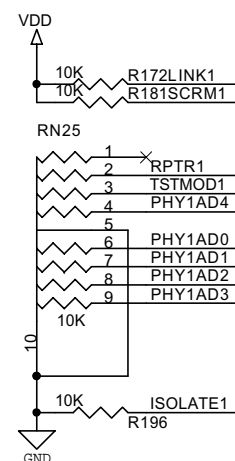
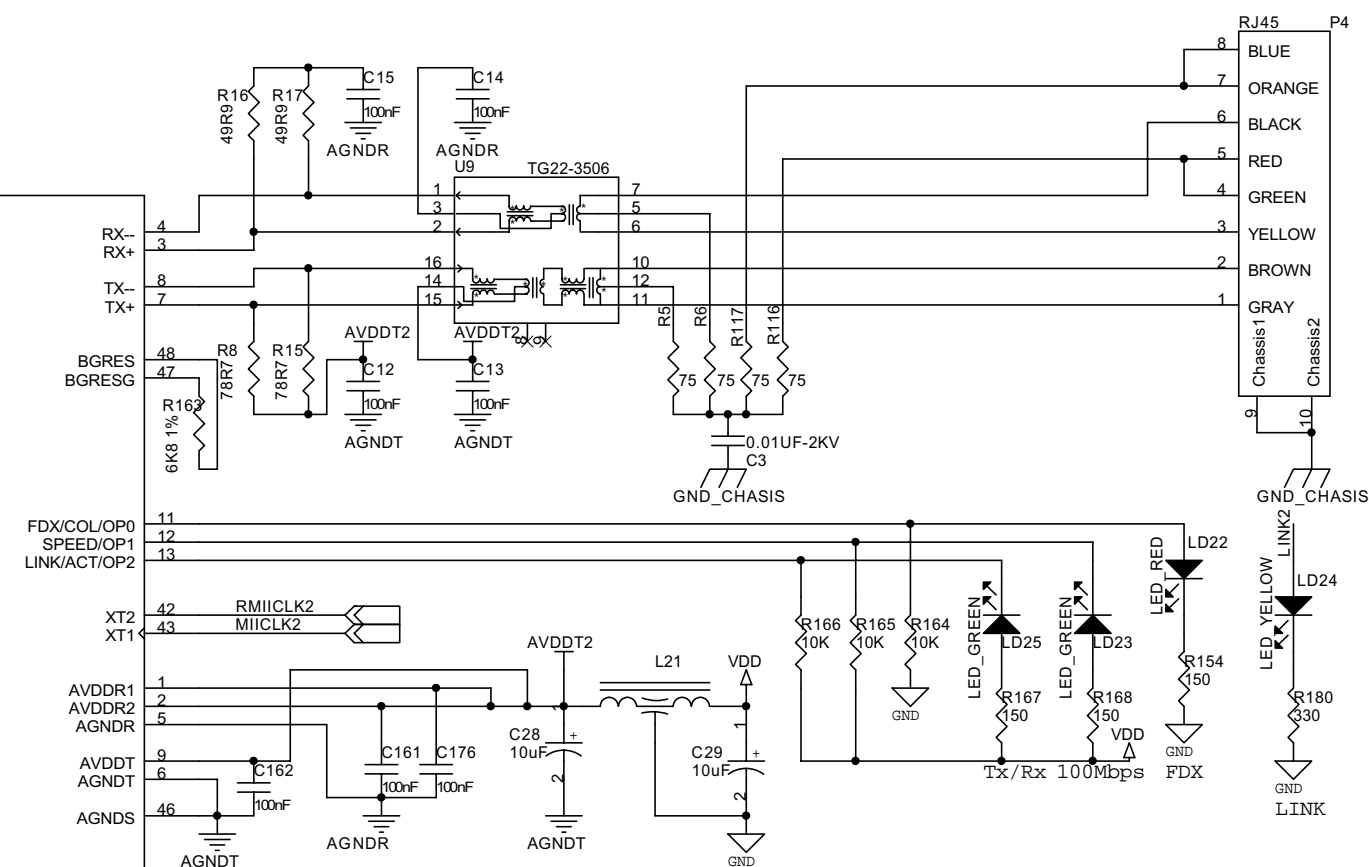
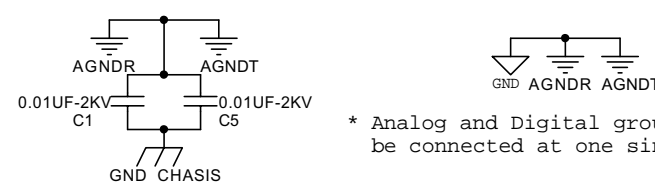
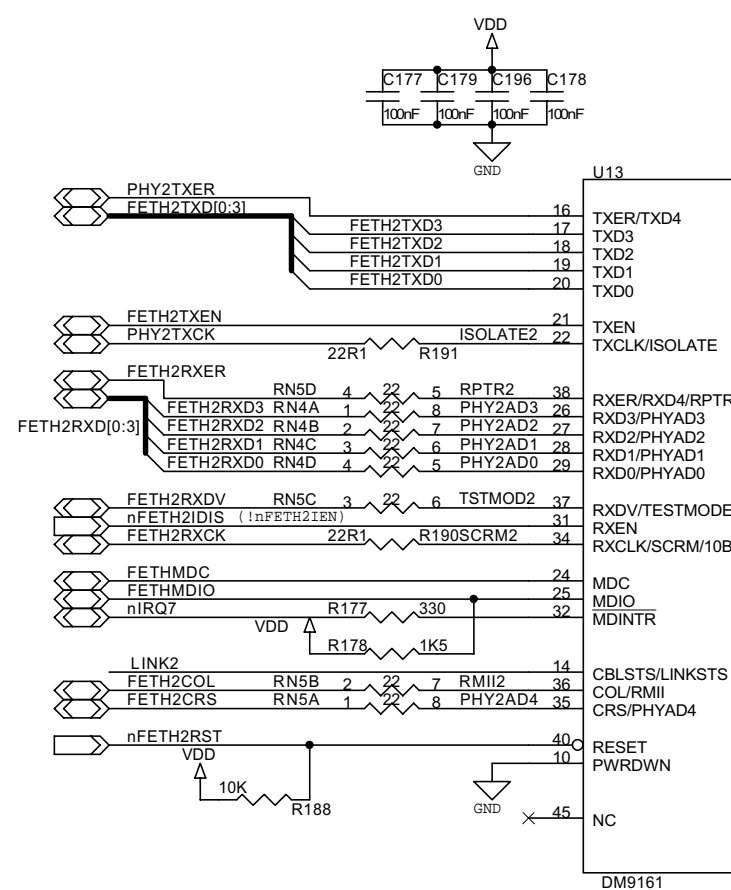
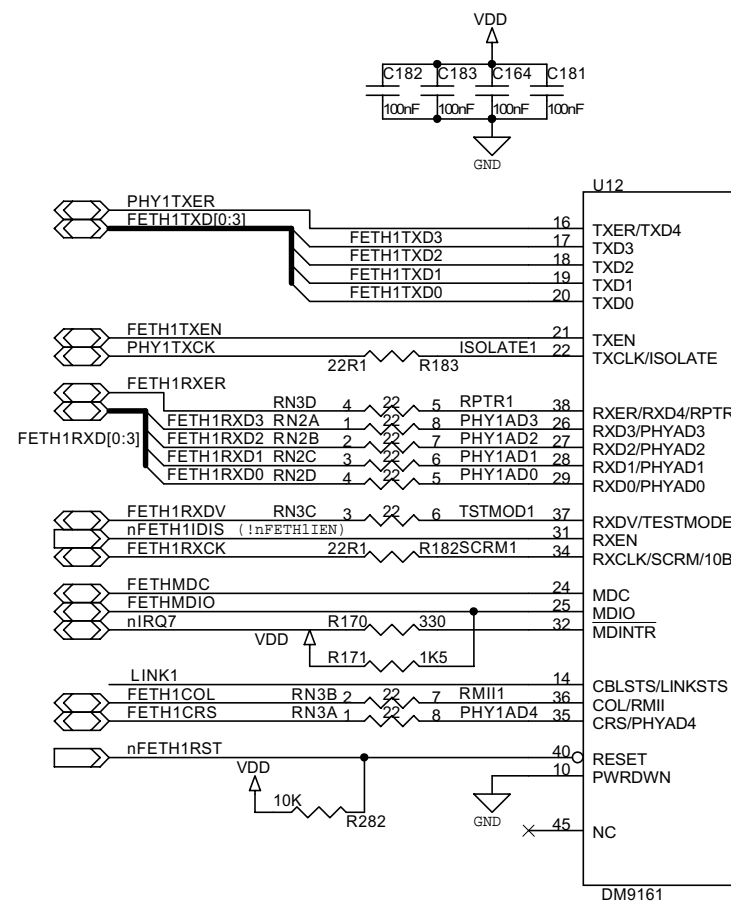
ATM MULTI PHY AND UTOPIA 16 BIT SUPPORT



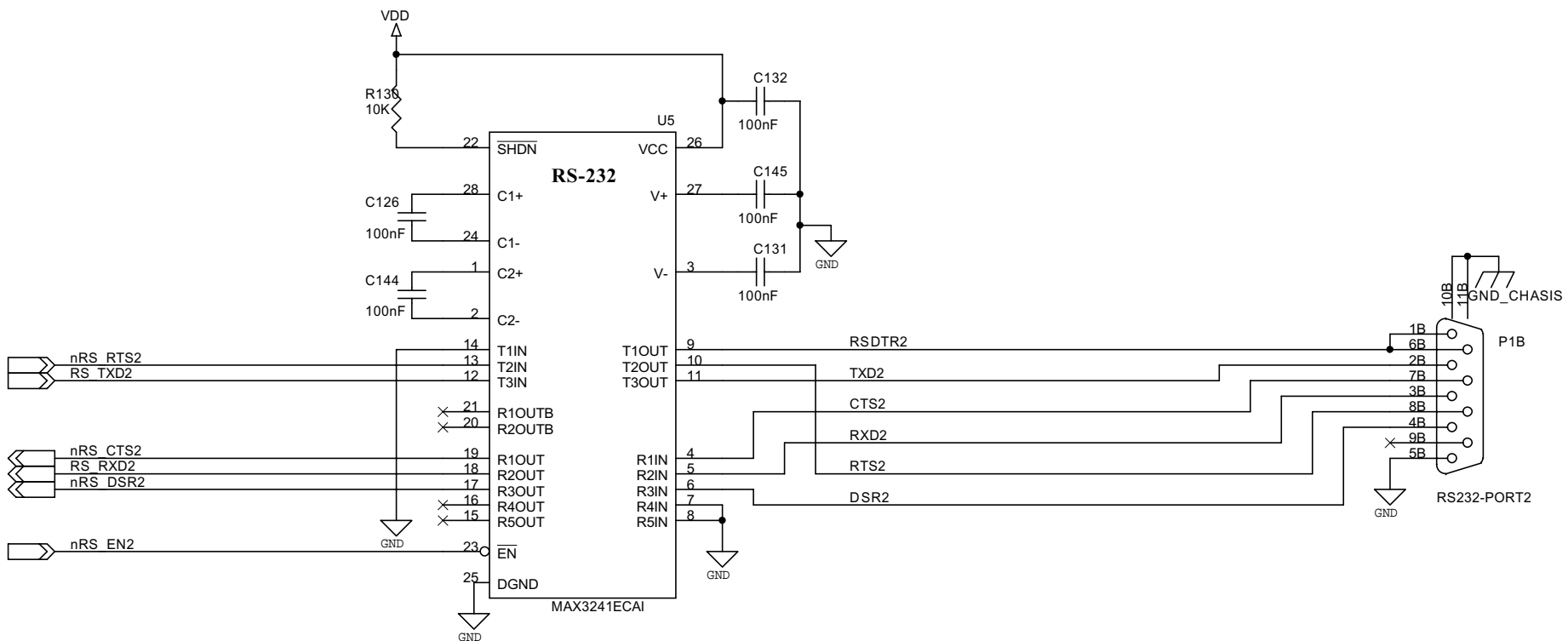
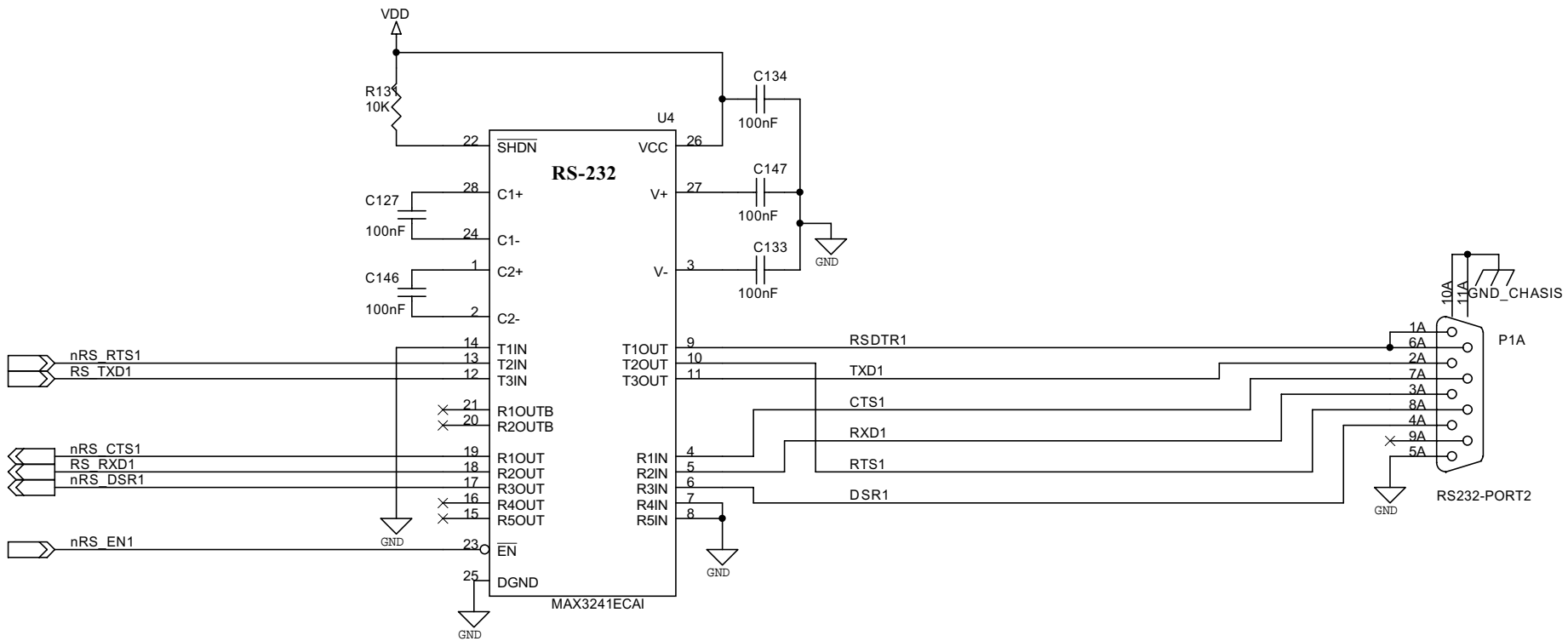
USB, RS232-1 and RS232-2 MUX

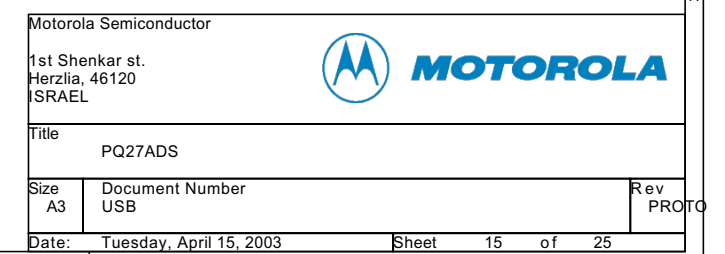


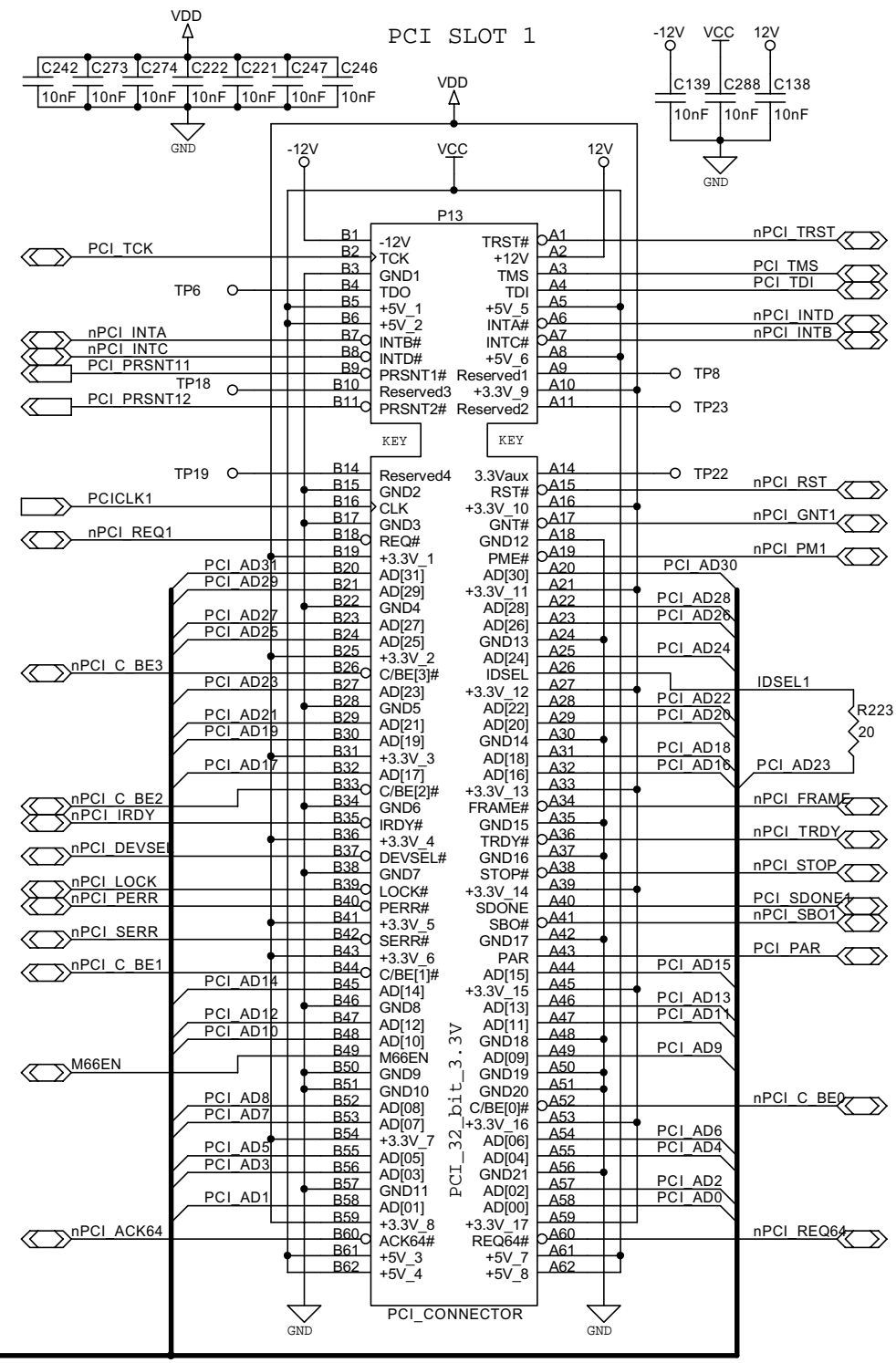
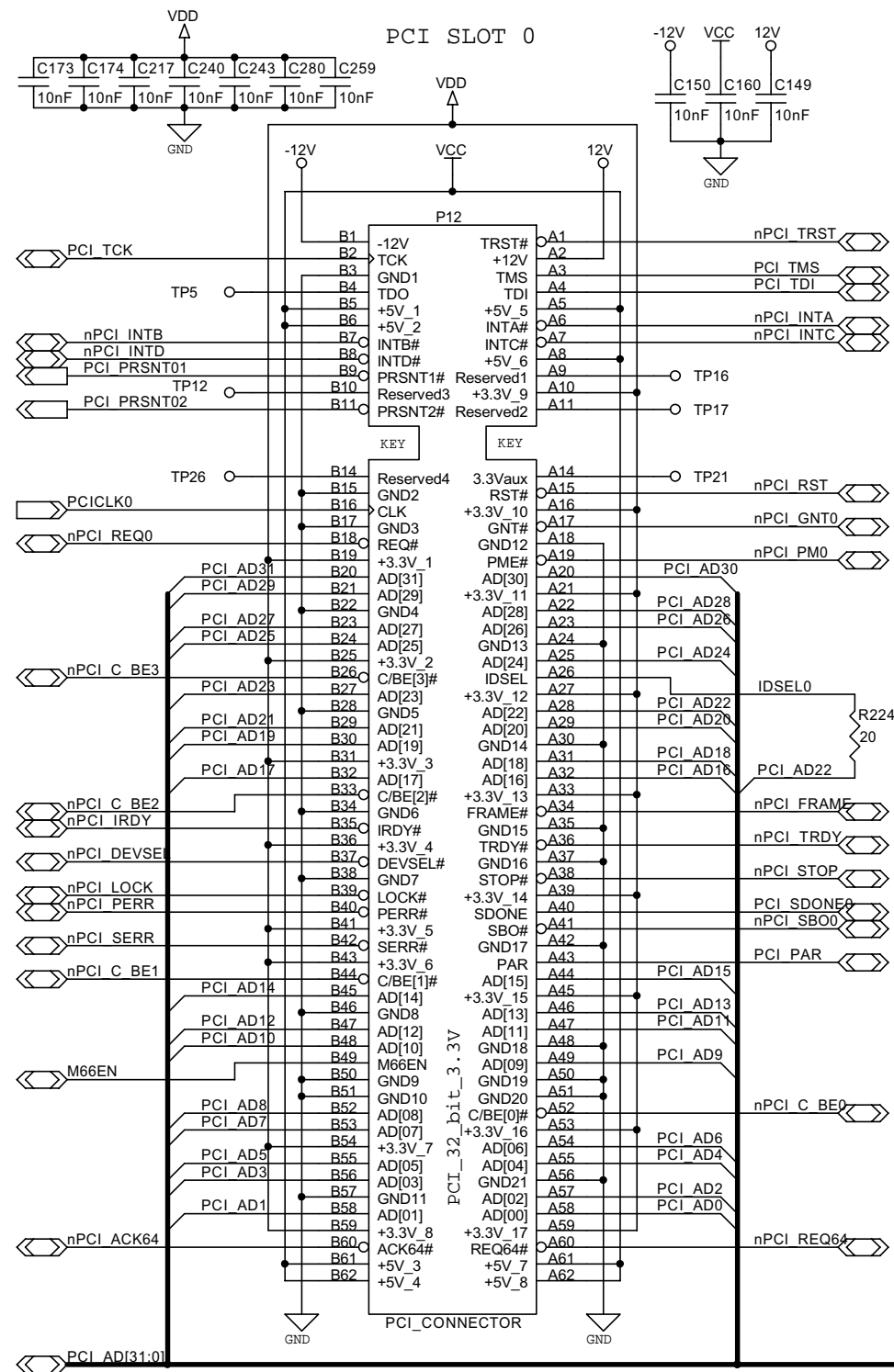


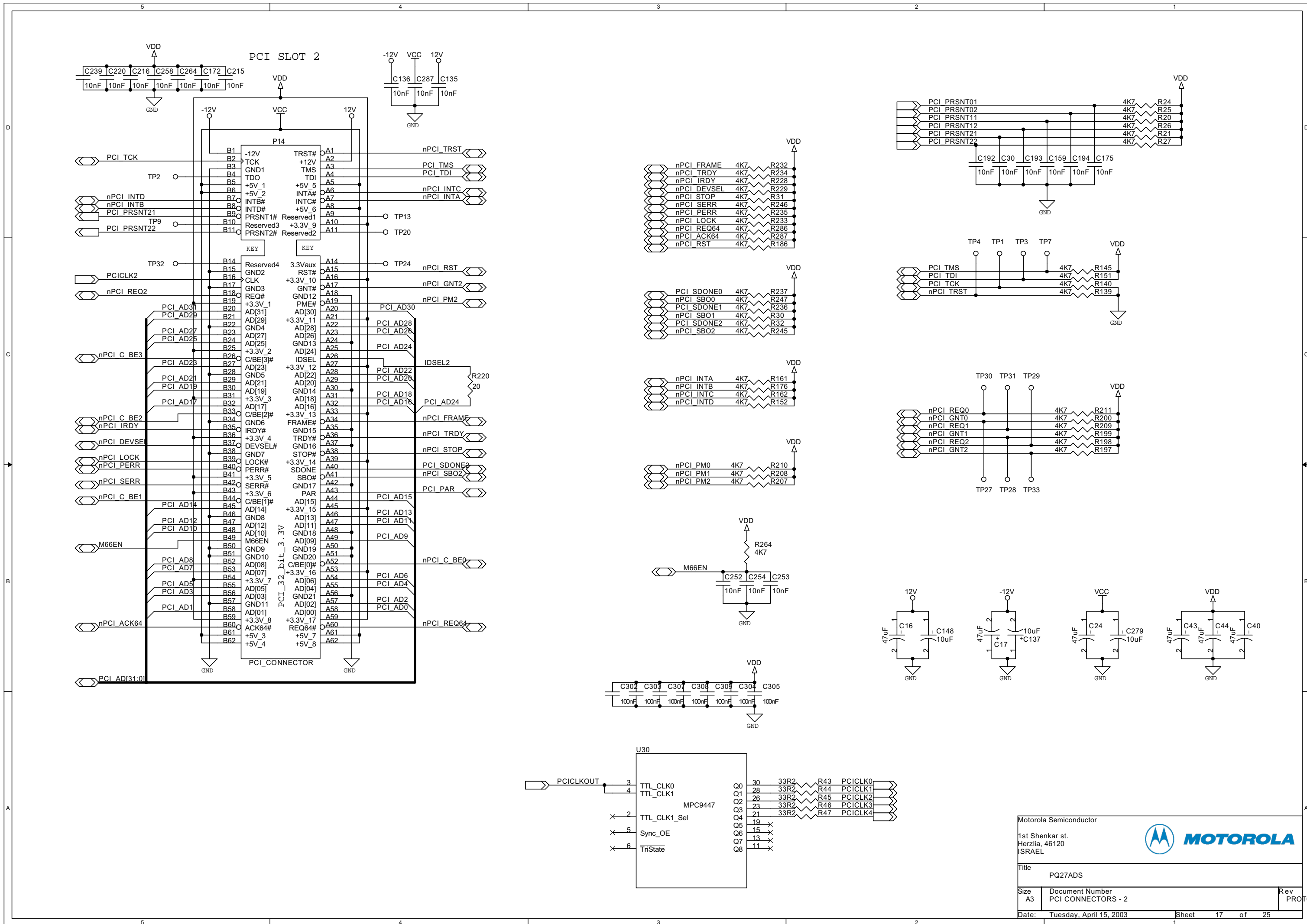
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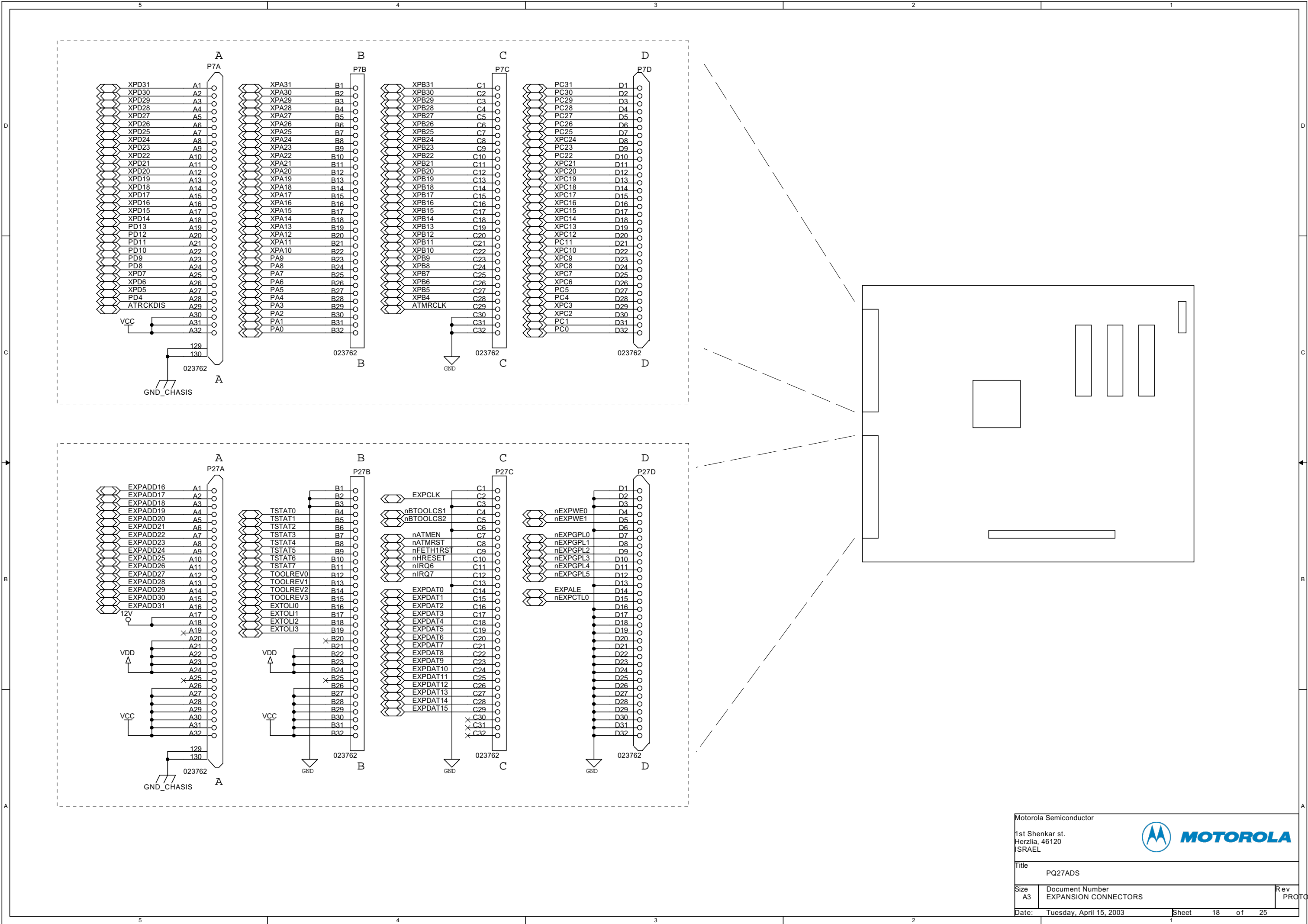
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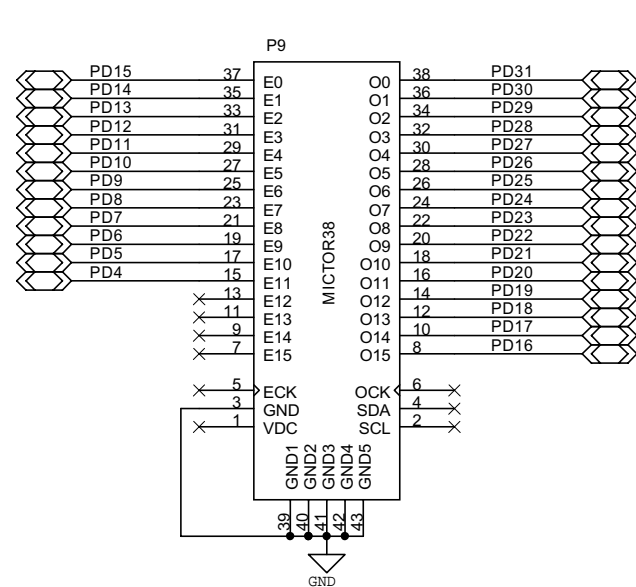
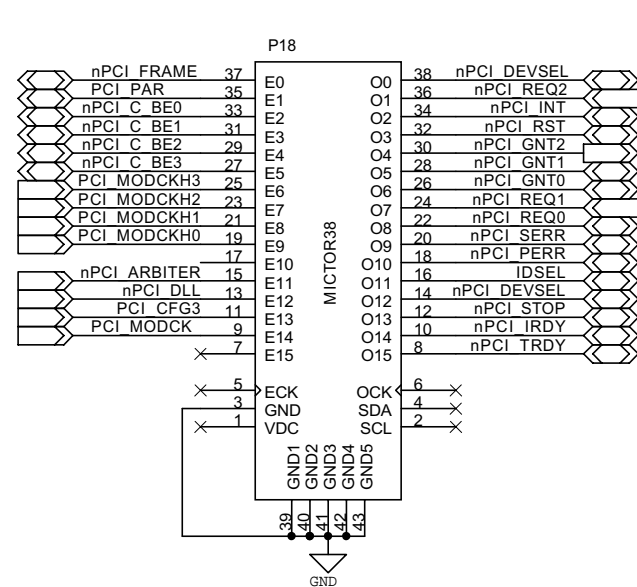
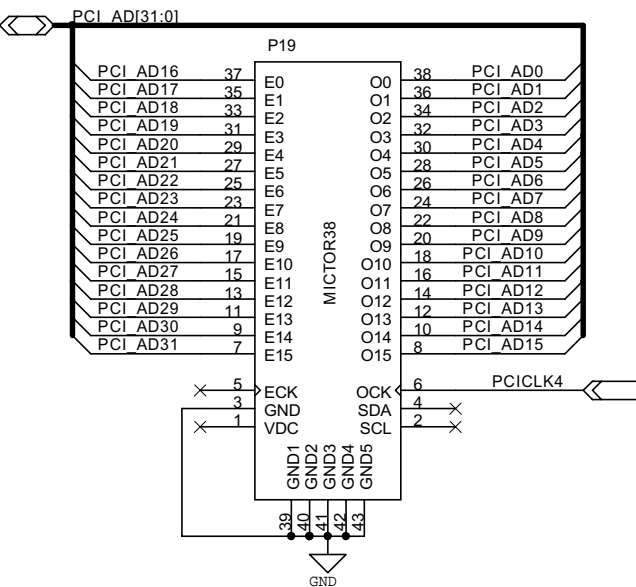
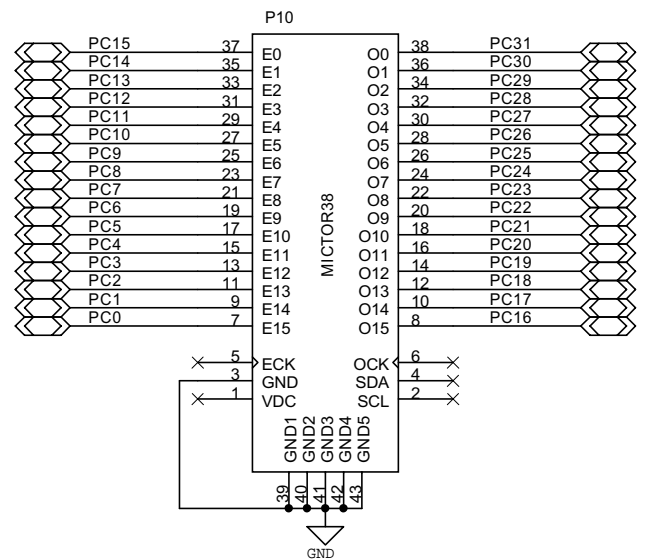
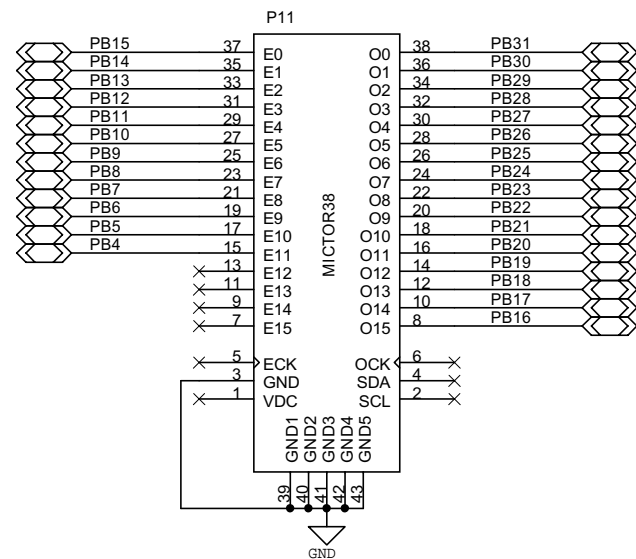
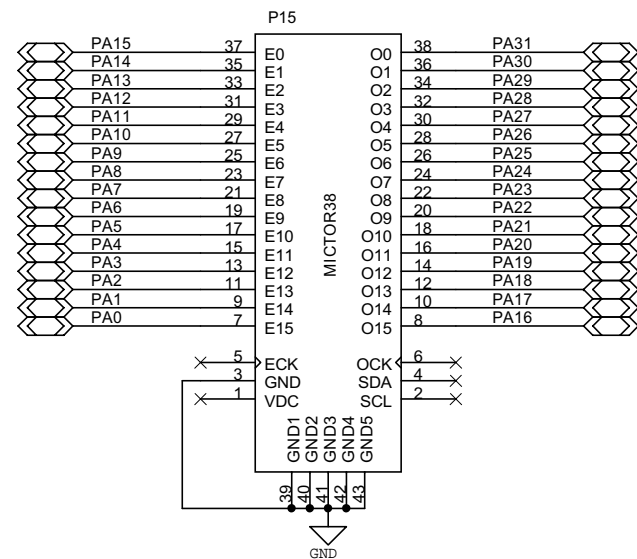
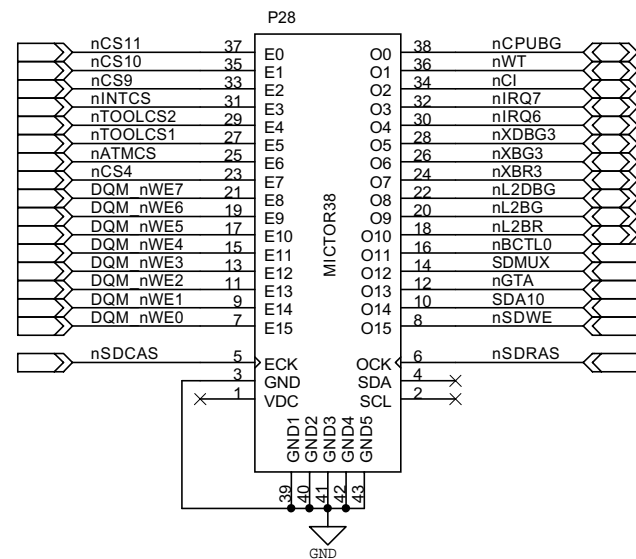
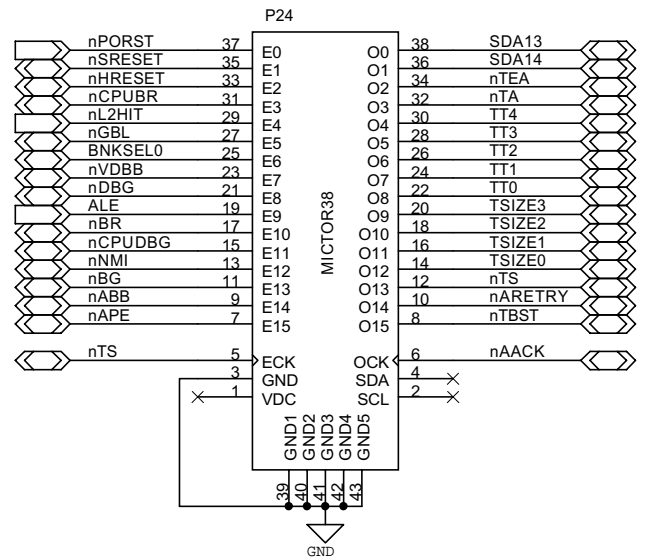
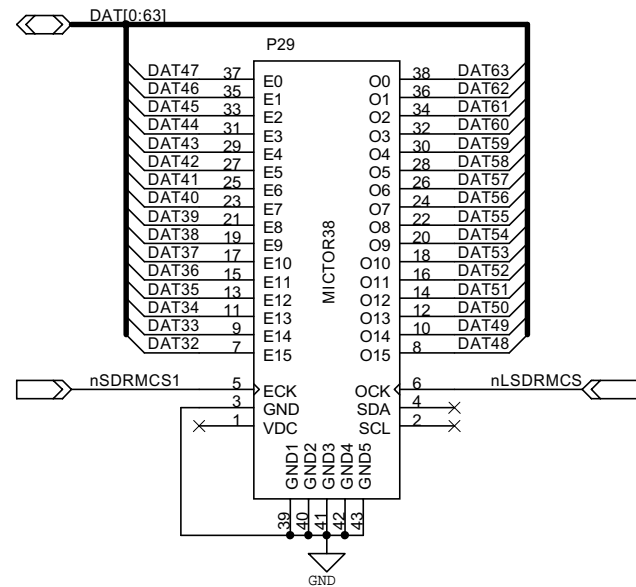
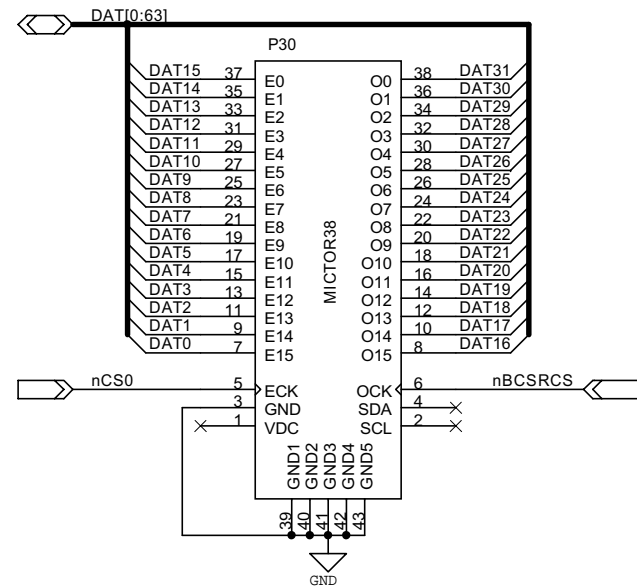
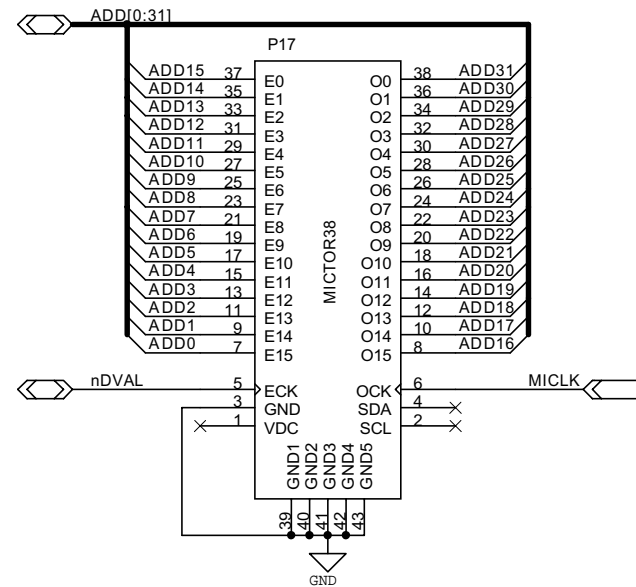




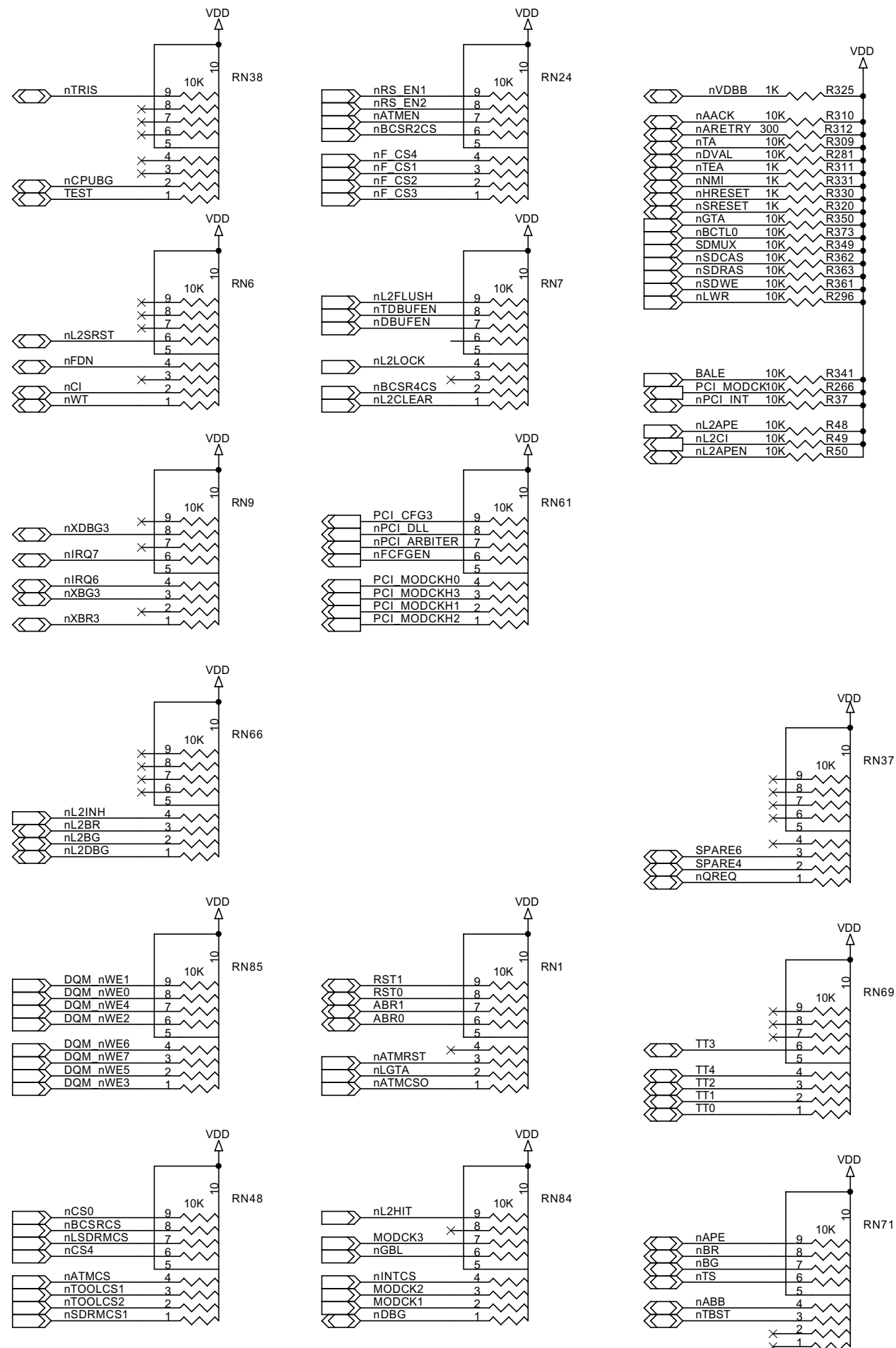




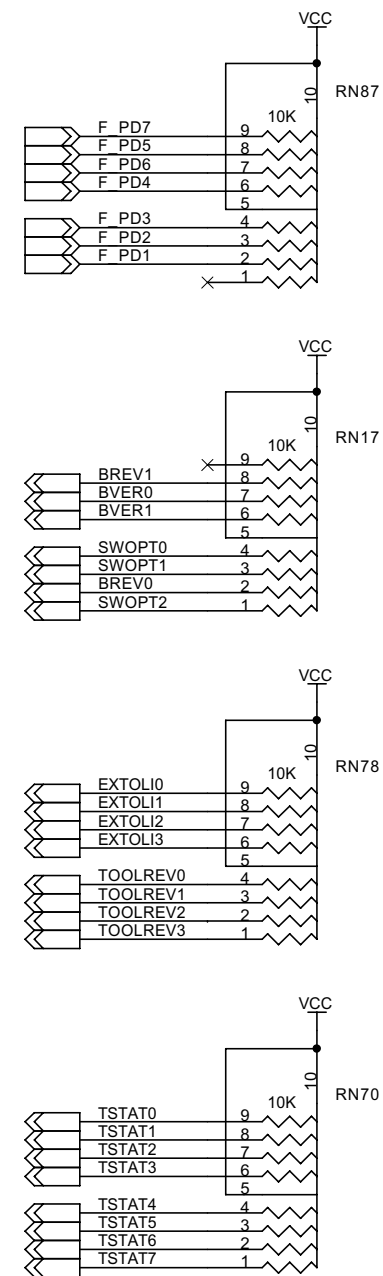




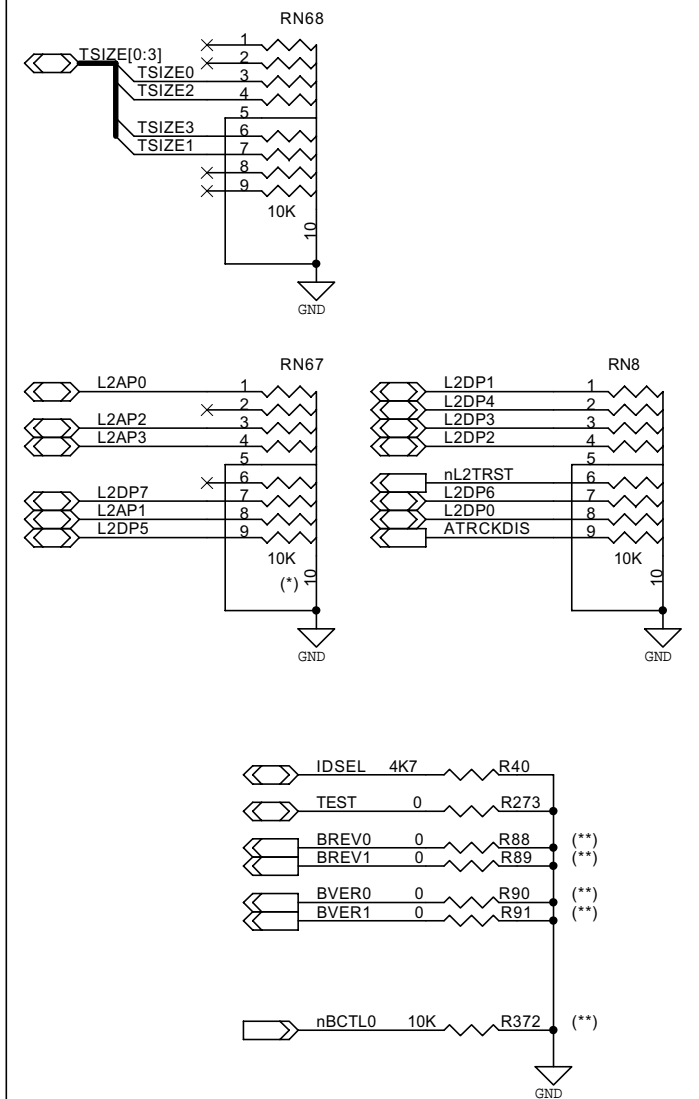
3.3V PULL-UPS



5V PULL-UPS



PULL-DOWNS



* RN67 is assembled if cache is used.

** R88 & R89 determine the boards' revision as follows:

R88	R89	Revision
yes	yes	ENG
no	yes	PILOT
yes	no	A
no	no	Reserved

** R372 depends on the functionality of nBCTL0 signal - not assembled in the factory.

(*) R210 Was changed to 300 ohm per device errata

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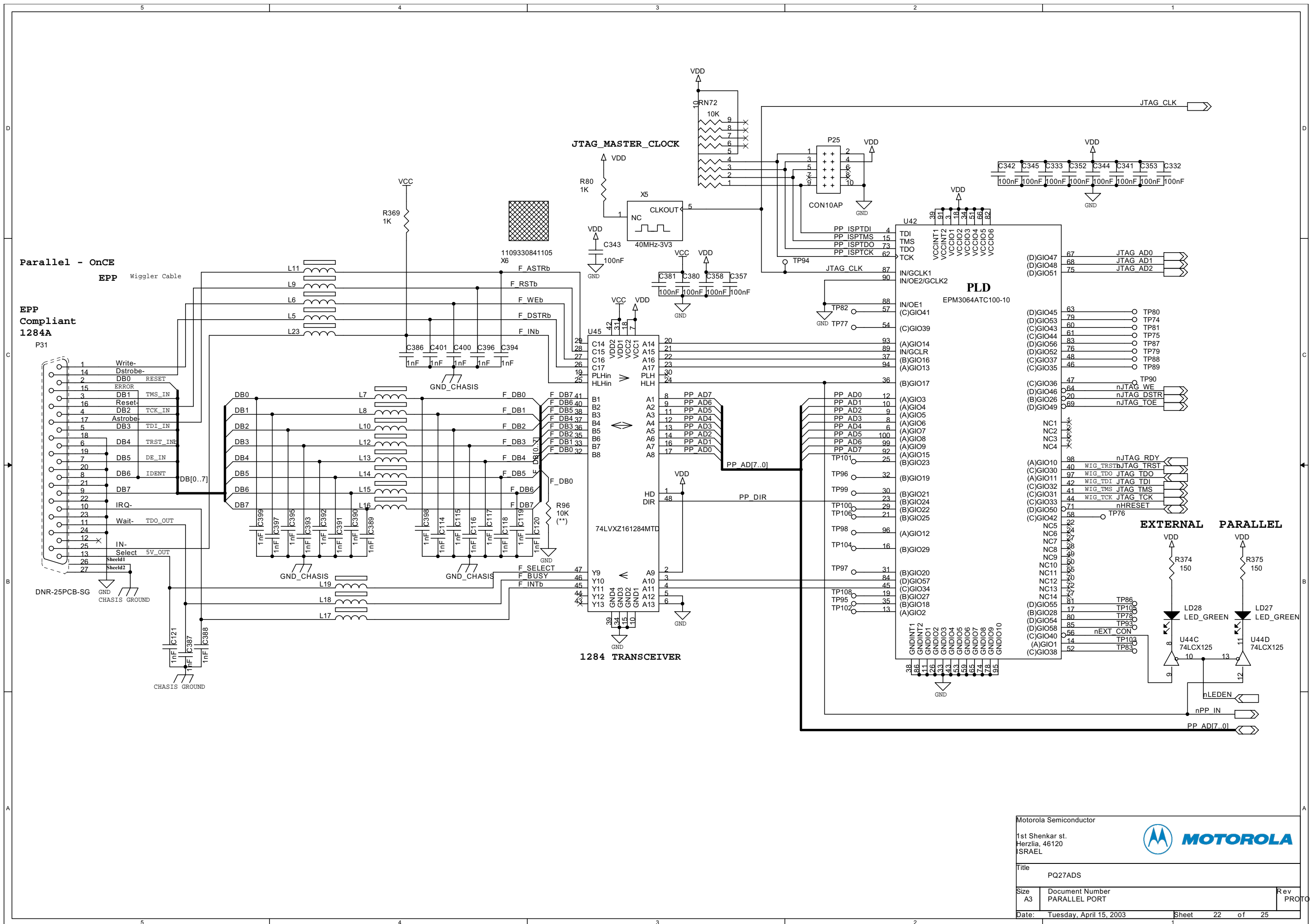
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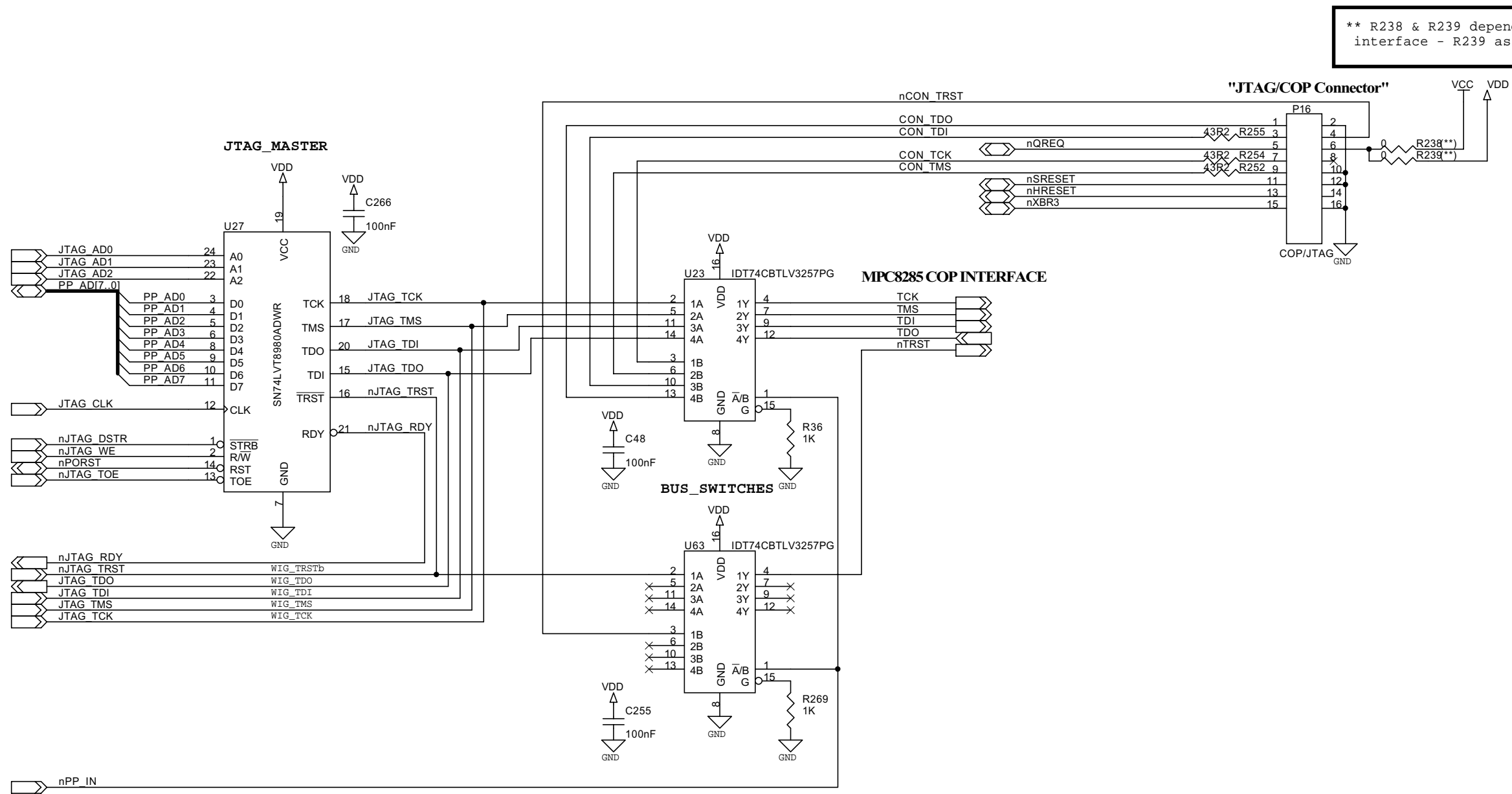
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A3
Document Number
PULL UPS/DOWNS

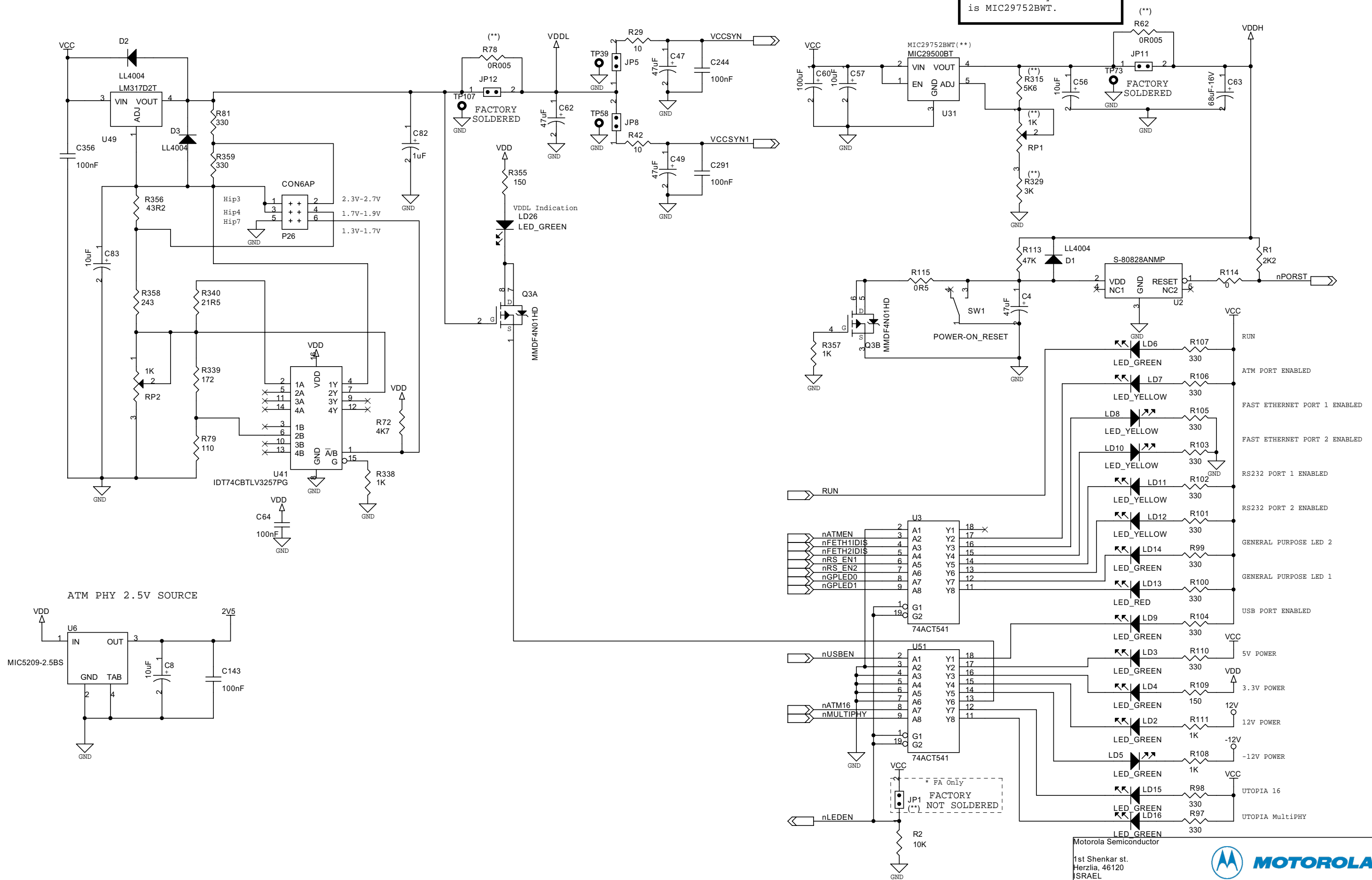
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