| Commercial & Industrial Qualification Report Summary              |  |   |  |  |  |  |  |  |
|---|--|---|--|--|--|--|--|--|
| Objective: Kirin2E 4th Revision for ADC drop and Hitch p          | problem  |   |  |  |  |  |  |  |
| Freescale PN: MCF52235CAL60<br>Part Name: Kirin 2E                | Customer Name(s): Various<br>PN(s): Various  | Plan or Results:<br>Revision # & Date:<br>See Revision History in Notes)                    |  |  |  |  |  |  |
| Technology: 0.25um SGF<br>Package: LQFP 112 20*20*1.4P0.65 (8255) | Design Engr:<br>Phone #: Bishnoi Navin-B12218  | QUARTZ Tracking #: 212944   |  |  |  |  |  |  |
| Fab / Assembly /<br>Final Test Sites: TSMC11/KLM/KLM              | Product Engr: Ad Azhar Muslim-B36772<br>Phone #: 03-78732507   | (Signature/Date shown below may be electronic)  |  |  |  |  |  |  |
| Maskset#: M23E<br>Rev#: 4   | Prod. Package Engr:<br>Phone #: not applicable   | PPE Approval (for<br>DIM/BOM results)<br>Signature & Date: not applicable, no change to BOM |  |  |  |  |  |  |
| Die Size (in mm)<br>W x L x T 7.150 x 6.940 mm                    | NPI PRQE: Miza Ismail - r27786 / Nurazah Ahmad-R63712<br>Phone #: 603 78732723                             | Miza Ismail - r27786 / Nurazah-R63712<br>NPI PRQE Approval Signature & Date: 3 Feb 2012     |  |  |  |  |  |  |
| Part Operating<br>Temp. Grade: Grade 3 -40 °C to +85 °C           | LOT A<br>WEMHA1DR3R0 LOT B LOT C<br>0 WEMHA1DR3U00 WEMHA1DR3T00<br>Trace/DateCode: QAA1112 QAD1112 QAC1112 | CAB Approval 10181374M<br>Signature & Date: 3 Feb 2012                                      |  |  |  |  |  |  |
|   |  | Customer Approval<br>Signature & Date: not required   |  |  |  |  |  |  |

(see Instruction #8 for use of rows 10-13; see examples below)

## TESTS HIGHLIGHTED IN YELLOW WILL BE PERFORMED FOR THIS STUDY

This preliminary qual plan is provided for planning purposes only and may be modified until Freescale CAB and customer approved.

|             | This testing is performed by Freescale Reliability Lab (KLM) unless otherwise noted in the Comments. |  |                               |  |               |                                    |  |   |  |
|-------------|--|--|-------------------------------|--|---------------|------------------------------------|--|---|--|
|             |  |  | GROUP A - ACCE                | LERATED ENVI   | RONMENTAL STR | RESS TESTS                         |  |   |  |
| Stress Test | Reference  |  | End Point<br>Requirements     | Minimum<br>Sample Size<br>(Note 1)   | # of Lots     | Total Units<br>including<br>spares | Results<br>Lot ID-(#Rej/SS)<br>NA=Not Applicable | Comments<br>(Note 2)  |  |
| PC          | JESD22-<br>A113<br>J-STD-020   | Preconditioning (PC) :<br>PC required for SMDs only.<br>MSL 3@ 260 °C, +5/-0 °C  | TEST @ RH                     | All surface mount devices prior to THB, HAST, AC, UHST,<br>TC, PC+PTC and as required per test conditions. |               |                                    | pass   |   |  |
| HAST        | JESD22-<br>A101<br>A110  | Highly Accelerated Stress Test (HAST):<br>PC before HAST (for SMDs only): Required<br>HAST = 130°C/85%RH for 96 hrs.<br>Bias = Vddmax<br>Timed RO of 48hrs. MAX  | TEST @ RH                     | 77   | 0             | 0                                  | pass   | 2M23E/112 LQFP 0/100 (Lot A*:<br>Quartz 48537-42 (limited test))<br>2M23E/112LQFP 0/100 (Lot A^: Quartz<br>48537-43 (sid test flow))<br>1M23E/112LQFP 0/231 |  |
| AC          | JESD22-<br>A102<br>A118  | Autoclave (AC):<br>PC before AC (for SMDs only): Required<br>AC = 121°C/100%RH/15 psig for 96 hrs<br><i>Timed RO of 2-48hrs. MAX</i>   | TEST @ R                      | 77   | 0             | 0                                  | not applicable                                   | not required  |  |
| тс          | JESD22-<br>A104<br>AEC Q100-<br>Appendix 3   | Temperature Cycle (TC):<br>PC before TC (for SMDs only): Required<br>TC = $65^{\circ}$ C to $150^{\circ}$ C for 500 cycles.<br>WBP after TC on 5 devices from 1 lot; 2 bonds<br>per corner and one mid-bond per side on each<br>device. Record which pins were used. | TEST @ RHC<br>WBP =/> 3 grams | 77   | 0             | 0                                  | pass   | 1M23E/112LQFP 0/77<br>Generic 112LQFP Data: 0/231<br>1M23E/80LQFP 0/231<br>1M23E/121MAPBGA 0/308  |  |
| HTSL        | JESD22-<br>A103  | High Temperature Storage Life (HTSL):<br>150 °C for 1008 hrs<br>CKBD condition prior to HTSL<br><i>Timed RO = 96hrs. MAX</i>   | TEST @ RHC                    | 77   | 0             | 0                                  | pass   | 1M23E/112LQFP 0/77 @ 504hrs/175C<br>1M23E/80LQFP 0/45 @ 1008h/150C<br>1M23E/80LQFP 0/45 @ 504h/175C<br>(FYI)<br>1M23E/121MAPBGA 0/154 @<br>1008h/150C       |  |

|              |                    | т  | EST GROUP B - A                         | CCELERATED L                        | IFETIME SIMUL | ATION TESTS                        |  |  |
|--------------|--------------------|--|---|-------------------------------------|---------------|------------------------------------|--|--|
| Stress Test  | Reference          | Test Conditions  | End Point<br>Requirements               | Minimum<br>Sample Size<br>(Note 1)  | # of Lots     | Total Units<br>including<br>spares | Results<br>Lot ID-(#Rej/SS)<br>NA=Not Applicable | Comments<br>(Note 2)   |
| HTOL (Vcore) | JESD22-<br>A108    | High Temperature Operating Life (HTOL):<br>Ta = 125°C for 168hrs<br>Bias =3.3V(Core)<br>10k W/E at 85C required prior to HTOL  | TEST @ RHC                              | 77                                  | 3             | 231                                | Lot A- 0/77<br>Lot B - 0/77<br>Lot C- 0/77       |  |
| HTOL (Vio)   | JESD22-<br>A108    | High Temperature Operating Life (HTOL):<br>Ta = 125°C for 168hrs (<br>Bias =3.8V (IO)<br>10k W/E at 85C required prior to HTOL   | TEST @ RHC                              | 77                                  | 3             | 231                                | Lot A- 0/77<br>Lot B - 0/77<br>Lot C- 0/77       |  |
| ELFR         | AEC Q100-008       | Early Life Failure Rate ELFR):<br>Ta = 125 °C for 48 hrs;<br>Bias : Core = 3.3V<br>Timed RO of 48 hrs MAX  | TEST @ RHC                              | 800                                 | 0             | 0                                  | pass   | 1M23E/112LQFP 0*/1155<br>* MiM Fails. Prior to MiM screen.<br>1M23E/112LQFP 0/900 with MiM<br>screen.<br>MCF52110(Kirin0, M44G) Q73527 -<br>0/800 @ 48hrs<br>MCF52221(Kirin2u, M24E) Q62036 -<br>0/811 @ 48hrs |
| EDR          | AEC Q100-005       | NVM Endurance, Data Retention, and<br>Operational Life (EDR):<br>DRB : 150C @ 168hrs<br>Timed RO of 48hrs. MAX   | TEST @ RHC                              | 77                                  | 0             | 0                                  | pass   | 1M23E/112LOFP 0/77 @ 504hrs/175<br>1M23E/80LQFP 0/45 @ 1008h/150C<br>1M23E/80LQFP 0/45 @ 504h/175C<br>(FYI)<br>1M23E/121MAPBGA 0/154 @<br>1008h/150C   |
|              |                    |  | TEST GROUP C                            | - PACKAGE ASS                       | EMBLY INTEGR  | RITY TESTS                         |  |  |
| Stress Test  | Reference          | Test Conditions  | End Point<br>Requirements               | Minimum<br>Sample Size<br>(Note 1)  | # of Lots     | Total Units<br>including<br>spares | Results<br>Lot ID-(#Rej/SS)<br>NA=Not Applicable | Comments<br>(Note 2)   |
| WBS          | AEC Q100-001       | Wire Bond shear (WBS)  | Cpk = or > 1.67                         | 30 bonds<br>from minimum 5<br>units | 0             | 0                                  | not applicable                                   | 1M23E/112LQFP 0/6, Cpk = 2.17<br>1M23E/80LQFP 0/6, Cpk = 4.24<br>1M23E/121MAPBGA 0/6, Cpk = 5.68   |
| WBP          | MilStd883-<br>2011 | Wire Bond Pull (WBP):<br>Cond. C or D  | Cpk = or > 1.67                         | 30 bonds<br>from minimum 5<br>units | 0             | 0                                  | not applicable                                   | 1M23E/112LQFP 0/6, Cpk = 4.22<br>1M23E/80LQFP 0/6, Cpk = 3.84<br>1M23E/121MAPBGA 0/6, Cpk = 2.59   |
| SD           | JESD22-<br>B102    | Solderability (SD):<br>8hr.(1 hr. for Au-plated leads) Steam age prior to<br>test.<br>If production burn-in is done, samples must also<br>undergo burn-in prior to SD. | >95% lead coverage<br>of critical areas | 15                                  | 0             | 0                                  | not applicable                                   | 1M23E/112LQFP 0/15<br>1M23E/80LQFP 0/15  |
| PD           | JESD22-<br>B100    | Physical Dimensions(PD):<br>PD per FSL 98A drawing   | Cpk = or > 1.67                         | 30                                  | 0             | 0                                  | not applicable                                   | 1M23E/112LQFP 0/30, All Cpks ><br>1.67<br>1M23E/80LQFP 0/30, All Cpks > 1.67   |

|                          |  |  | TEST GROUP  | E - ELECTRIC                       | AL VERIFICATIO | N TESTS                            |   |   |
|--------------------------|--|--|---|------------------------------------|----------------|------------------------------------|---|---|
| Stress Test              | Reference                              | Test Conditions  | End Point<br>Requirements   | Minimum<br>Sample Size<br>(Note 1) | # of Lots      | Total Units<br>including<br>spares | Results<br>Lot ID-(#Rej/SS)<br>NA=Not Applicable  | Comments<br>(Note 2)  |
| НВМ                      | JEDEC<br>METHOD (AEC<br>NOT REQ'D)     | ElectroStatic Discharge/<br>Human Body Model Classification (HBM):<br>Test @ 500/1000/1500/2000 Volts<br>See AEC-Q100-002 for classification levels.   | TEST @ RH<br>2KV min.   | 3 units per<br>Voltage level       | 1              | 12                                 | WEMHA1DR3R00<br>0/3 @ 500V<br>0/3 @ 1000V<br>0/3 @ 1500V <sup>Note 1</sup><br>3/3 @ 2000V <sup>Note 2</sup> | Note 1: Unit go through substitute<br>method as allowed by AEC Q100G.<br>Note 2: Equivalent result with 3M23E<br>Refer datasheet for details. |
| ММ                       | AEC-Q100-003                           | ElectroStatic Discharge/<br>Machine Model Classification m(MM):<br>Test @ 50/100/200 Volts<br>See AEC-Q100-003 for classification levels.  | TEST @ RH<br>200V min.  | 3 units per<br>Voltage level       | 0              | 0                                  |   | not required  |
| CDM                      | JEDEC<br>METHOD (AEC<br>NOT REQ'D)     | ElectroStatic Discharge/<br>Charged Device Model Classification (CDM):<br>Test @ 250/500/750cP Volts<br>See AEC-Q100-011 for classification levels.<br><i>Timed RO of 96hrs MAX</i> .  | TEST @ RH<br>Corner pins =/><br>750V;<br>All other pins =/><br>500V | 3 units per<br>Voltage level       | 1              | 9                                  | Lot A :<br>250V: 0/3<br>500V: 0/3<br>750V: 0/3  |   |
| LU                       | JESD78<br>plus<br>AEC-Q100-<br>004     | Latch-up (LU):<br>Test per JEDEC JESD78 with the AEC-Q100-004<br>requirements.<br>Ta= Maximum operating temperature<br>Vsupply = Maximum operating voltage   | TEST @ RH   | 6                                  | 1              | 6                                  | Lot A - 0/6   |   |
| ED (Vcore)               |  | Electrical Distribution (ED)<br>pre and post 168hrs HTOL   | TEST @ RHC  | 30                                 | 3              | 90                                 | Pass  |   |
| FG                       | AEC-Q100-007                           | Fault Grading (FG)   | FG shall be = or ><br>90% for qual units                            |                                    |                |                                    |   | not required  |
| GL                       | AEC-Q100-006                           | Electro-Thermally Induced Gate Leakage (GL):<br>155°C, 2.0 min, +400/-400 V<br>Timed RO of 96 hrs MAX.<br>For all failures, perform unbiased bake<br>(4hrs/125°C, or 2hrs/150°C) and retest;<br>recovered units are GL failures. | TEST @ R  | 6                                  | 0              | 0                                  |   | not required  |
| EMC                      | SAE J1752/3 -<br>Radiated<br>Emissions | Electromagnetic Compatibility (EMC)<br>(see AEC Q100 Appendix 5 for test applicability;<br>done on case-by-case basis per<br>customer/Freescale agreement)   | <40dBuV<br>150kHz - 1GHz  | 1                                  | 0              | 0                                  |   | not required  |
| Qualification Materia    | al :                                   |  |   |                                    |                |                                    |   | -   |
| Part Number              | Moo Number                             | Die Size, Process Technology   | Quartz  | Wafer Fab/<br>Polymide             | Die Attach     | Mold Compound                      | Leadfram Flagsize   | Wire  |
| Kirin 2E                 | M23E                                   | 7.150 x 6.940 mm , E025AFXQ  | 212944  | TSMC11/ 8124                       | CRM 1525       | SUMITOMO G760                      | Substrate 12x12   | 0.98mils  |
| evision history<br>Revis | ion                                    | Date   |   |                                    |                | omment                             |   | Author  |
|                          |  |  |   |                                    | 0              | ommont                             |   |   |