# Model-Based Design Toolbox MPC57xx Series

**Release Notes** 

An Embedded Target for the MPC57xx Family of Processors Version 3.0.0

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# 1 What is New in Version 3.0.0

The latest <u>NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0</u> is designed to support multiple MPC57xx microcontrollers and brings the following important enhancements compared with previous version of this toolbox (v.2.0.0)

- v.3.0.0 supports **3 new processor series and 12 new derivatives**. The new processors and derivatives are listed below:
  - MPC5741**P** (*previous MPC5742***P**, *MPC5743***P**, *MPC5744***P**)
  - MPC5744**B**, MPC5745**B**, MPC5746**B**
  - MPC5744**C**, MPC5745**C**, MPC5746**C**, MPC5747**C**, MPC5748**C**
  - MPC5746**G**, MPC5747**G**, MPC5748**G**
- v.3.0.0 supports the following MPC57xx Motherboards and DEVKITS:
  - DEVKIT-MPC5744P PCB RevX1 SCH RevB
  - DEVKIT-MPC5748G PCB RevA SCH RevB
  - Daughter Card MPC574XG-256DS RevB
  - Daughter Card X-MPC574XG-324DS RevA
  - Daughter Card MPC5744P-257DS RevB1
  - Daughter Card SPC5746CSK1MKU6
  - Motherboard X-MPC574XG-MB RevD
  - o Motherboard MPC57XX RevC
- Enhanced user experience with a complete redesign of all **Simulink Library blocks** to support:
  - o Same look&feel with Model-Based Design Toolbox for S32K14x Series
  - o Basic and Advanced configurations modes based on SDK standard API
  - Multiple pin packages options
  - Multiple peripheral instances
  - $\circ\,$  Integration with MATLAB Installer, Help and MathWorks online Add-on Manager for distribution and installation
- v.3.0.0 integrates the latest PA SDK version 2.0.0 RTM (December 2018) for NXP MPC574x-B-C-G, MPC574x-P MCU. NXP's Model-Based Design Toolbox generates codes based on standard SDK API, covering most of the functionalities exposed by SDK;
- v.3.0.0 integrates the Automotive Math and Motor Control Library release 1.1.15 (January 2019) for PA e200 cores;
- Compatible with **S32 Design Studio 2017.R1 for Power Architecture** and **GCC 4.9**. The generated code from MATLAB/Simulink can imported and build as projects in S32 Design Studio, Downloaded and Debug;
- Enable MATLAB Profiler support for PIL/SIL targets;

- Expand the example library to cover all Simulink Blocks provided as part of the NXP Toolbox for MPC57xx devices. The **example library contains 102 examples** that covers a wide range of topics like:
  - I/O control: GPIO, Sine Wave Generator (SWG)
  - Timers: Programmable (PIT), eTimer
  - Motor Control: Pulse Width Modulation (PWM), Cross Triggering Unit (CTU), Analogue Converter (ADC)
  - o Communication: CAN, SPI, I2C, UART
  - Core & Systems: DMA, Registers
  - Software-in-the-Loop (SIL) and Processor-in-the-Loop (PIL)
  - o SRAM and Flash Programming over UART
- **Motor Control examples** for PMSM and BLDC built on top of DEVKIT MPC5744P and MotorGD

The Simulink Models are saved in the NXP Toolbox root directory under MPC\_Examples folder and can be accessed easily from mbd mpc574x examples.mdl library file.



For more details about each of the topics highlighted above please refer to the following chapters.

# 2 MPC57xx MCU Support

This release is currently supporting only MPC574x B/C/G/P series. The support for MPC577x will be added after PA SDK RTM will become available for mass market.

## 2.1 Packages & Derivatives

<u>NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0</u> supports the following MCU series and packages:

- MPC574xP MCU Packages with 128/198/256/384KB SRAM:
  - 144 LQFP;
  - 256 BGA;
- MPC574xB MCU Packages with 192/256/384/512KB SRAM:
  - 176 LQFP;
  - 100 BGA;
  - 256 BGA;
  - 324 BGA;
- MPC574xC MCU Packages with 256/384/512/768KB SRAM:
  - 176 LQFP;
  - 100 BGA;
  - 256 BGA;
  - 324 BGA
  - 512 BGA;
- MPC574xG MCU Packages with 768KB SRAM:
  - 176 LQFP;
  - 256 BGA;
  - 324 BGA;

The toolbox supports operation with 40MHz external XTAL and MCU system clock configuration frequencies of 200MHz.

## 2.2 Peripherals & Devices

<u>NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0</u> supports the following peripherals and devices that are highlighted in **RED**:

- MPC574xP Ultra-Reliable MCU for Automotive & Industrial Safety Applications



 MPC574xB/C/G Ultra-Reliable MCUs for Automotive & Industrial Control and Gateway



# 3 Model-Based Design Toolbox Features

<u>NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0</u> is delivered with a complete MPC57xx MCU Simulink Block Library as shown below.

There are three main categories:

- MPC574x Core, System, Peripherals and Utilities which contains all blocks related with MCU configuration
- MPC574x Automotive Math and Motor Control which generic 16bit, 32bit and floating point single precision blocks
- MPC574x Example Projects which contains all the examples that exercise all the other blocks



**NOTE** In preparation for supporting multiple derivatives of MPC57xx series the Simulink Library for MPC574x has been redesigned. The Simulink models created with previous releases need be converted manually from Simulink to work with the newest version

## 3.1 New MBD MPC574x Configuration Block

For addressing the increased number of MCU and options the main toolbox configuration block has been completely redesigned. In the newest version of the <u>NXP's Model-Based Design</u> <u>Toolbox for MPC57xx version 3.0.0</u> this is a standalone block with its own user interface designed to address four main scenarios:

#### 3.1.1 MCU Support

The MCU Tab allows the configuration of the MPC574x options used for code generation. In this tab the selection of the MCU, Package and Clock have a global influence over the Simulink Model.

😼 Block Parameters: M	BD_MPC574x_Config_Information	>	<	Block Parameters: MBD_MPC574x_Config_Information	$\times$
MBDTBX_EC_MPC57	4X (mask) (link)		^	MBDTBX_EC_MPC574X (mask) (link)	1
Model Based Design	Toolbox Config block for MPC574x family of processors.			Model Based Design Toolbox Config block for MPC574x family of processors.	
MCU Build Toold	hain Target Connection Diagnostics			MCU Build Toolchain Target Connection Diagnostics	
Target MCU				Target MCU	
Family	MPC5744P	•		Family MPC5744P -	
Package	144-LQFP	•		Package MPC5741P	
Target Sram Size	384KB	•		Target Sram Size MPC5743P	
Clock				Clock MPC5744C	
Clock Frequency MH	iz 200	•		Clock Frequency MHz MPC5745B	
XTAL Frequency MH	z 40	•		MPC5746B XTAL Frequency MHz MPC5746C	
Step Tick				MPC5746G Step Tick MPC5747C	
Timer	PIT Channel 0	•		Timer MPC5747G	
Priority	1	•		Priority MPC5748G	
		_	~		Ι,
	OK <u>C</u> ancel <u>H</u> elp AF	ply		<u>O</u> K <u>C</u> ancel <u>H</u> elp <u>Appl</u>	y

**NOTE** When changing the MCU or package the I/O pins might be reset to defaults in case the corresponding pins are not available. Make sure you check your model after changing the MCU and/or package

#### 3.1.2 Target Connection

Connection with a NXP MPC57xx target is now easier. For both Serial and OpenSDA the MBD Toolbox has a discovery features that allows you to detect the ports automatically. Just click on Refresh button to get a list of available connections.

Block Parameters: N	IBD_MPC574x_Config_Infor	mation			×	
MBDTBX_EC_MPC57	74X (mask) (link)	- MDCE74- 6			· · · · · · · · · · · · · · · · · · ·	^
Model Based Design	Toolbox Config block to	r MPC574X Ta	amily of proces	sors.		
MCU Build Toold	hain Target Connect	ion Diagn	ostics			
	l oon (PII ) Mode Downl	oad				
Download Code	after Build	odd				
Download settings						
Delay before start o	of application (ms) 5000	)				
, Boot Assist Modu	ile (BAM) Restart Reque	st				
Serial						
COM Port	Custom	•	Refres	h		
Custom COM Port	COM1					
Baud Rate	115200				•	
Pil Settings						
PIL Interface	Serial 0				~	
Default Password	ł					
BAM Password	0xFEEDFACECAFEBEEF					
BAM Manual Star	t Address and Code Size	2				
BAM Start Address	0				:	
BAM Code Size	0				:	
						~
		<u>O</u> K	<u>C</u> ancel	<u>H</u> elp	<u>A</u> pply	

#### 3.1.3 **Diagnostics**

The Diagnostics panels allow various checking for the correct usage of the Simulink blocks and dependencies between them:

- Checks if pins are shares across various Simulink blocks within the model;
- Checks if incorrect instances of a configured peripheral are used;
- Consistency checks between the block options;

Block Parameters: MBD_MPC574x_Config_Information						
MBDTBX_EC_MPC574X (ma Model Based Design Toolbo	ask) (link) ox Config block for MP	C574x family of	processors.	î		
MCU Build Toolchain	Target Connection	Diagnostics				
Motor Control	I/O					
✓ ADC	GP	0				
✓ PWM	GP	I				
⊡ CTU	Core	and System				
⊡ SWG	⊡ eD	MA				
✓ eTimer		Г				
Communication	Utiliti	es				
CAN		IMCLIB				
I SPI	🗹 Fre	emaster				
	Pro	ofiler				
	Mc	del Reference				
I2C ⊻ I2C	Mc	del Global Pin C	heck			
				~		
	<u>O</u> K (	<u>C</u> ancel <u>H</u>	elp <u>A</u> pply			

#### 3.1.4 **New PWM Configuration Block**

To address complex scenarios the PWM configuration the block was redesigned to be in sync with SDK 2.0.0 RTM drivers. The new configuration block follows now the overall NXP MBDT design philosophy to have a single configuration block per each peripheral. From the new PWM configuration block you can:

- Configure PWM to operate in various modes
- Assign output and input pins
- Control output triggers

Block Parameters: FlexPWM	A Config		×
flexpwm mpc574x config	(mask) (link)		
FlexPWM Submodule Confi	iguration Bloc	k, which enables PWM signals on each FlexPWM Submodule.	
General Signal Satur	Output Tric	, S	
Submodule selection	Output mi	Juers	
Module 0		•	
Submodule 0		-	
Channels settings			
Mode Ce	enter aligned	PWM +	
Channel pair operation Co	omplementar	y -	
Complementary source P	WM23	¥	
PWM Frequency settings			
PWM Counter init	Su	b-module 0 master reload •	
PWM Clock source	Pe	ripheral clock •	
PWM Period expressed in	units of: Hz	T	
PWM Output Frequency (H	Hz) 10	000	
PWM Period Value (Ticks)	80	00	
Clock prescaler	Di	vide by 1 🔹	
PWM Deadtime settings			
PWM Deadtime expressed	l in units of:	Nanoseconds -	
PWM Deadtime (ns)	[	50	
PWM Deadtime (Ticks)	[	4	
Reload opportunities			
Reload logic H	Half Cycle	•	
Reload frequency	very 1 PWM c	ppportunity -	
Force triggering signal	ocal force sig	nal 🔹	
		OK Cancel Help App	ly

#### 3.1.5 New ADC Configuration Block

To address complex scenarios the ADC configuration the block was redesigned to be in sync with SDK 2.0.0 RTM drivers. The new configuration block follows now to perform advanced motor control applications based on HW interrupts and synchronization with PWM and CTU.

Block Parameters: ADC_Config	1	×			
-adc_mpc574x_config (mask)	(link)	^			
This block allows the user to	configure ADC instance.				
General Advanced Th	resholds Watchdog				
Settings					
ADC Instance	0	•			
Conversion Mode	One-shot	•			
CTU Control Mode	Trigger Mode	•			
Channels Selection					
Number of Normal Channels	5	•			
1st Channel	PTB7: [ADC0_AN0   Analog function AN0 of ADC0]	•			
2nd Channel	PTB9: [ADC0_AN11   Analog function AN11 of ADC0]	•			
3rd Channel	PTB7: [ADC0_AN0   Analog function AN0 of ADC0]	•			
4th Channel	PTB7: [ADC0_AN0   Analog function AN0 of ADC0]	•			
5th Channel	PTB7: [ADC0_AN0   Analog function AN0 of ADC0]	•			
Show advanced options					
		~			
	OK Cancel Help A	pply			

#### 3.1.6 **New REGISTER Support**

For unsupported peripherals, there is now an easy way to configure them from MATLAB/Simulink with the help of REGISTER READ/WRITE blocks. For each platform supported, a list of all available platform registers is available for read or write operations.

🚹 Block Par	rameters: Read_Register ×	Block Par	rameters: Read_Register	$\times$
-Read_Regi	ster (mask) (link)	-Read_Regi	ister (mask) (link)	
This block	allows direct access to read a register.	This block	allows direct access to read a register.	
Parameters	S	Parameter	S	
Peripheral	ADC_0 ·	Peripheral	ADC_0	-
Register	MCR -	Register	PLLDIG PMC	^
Register de	escription	Register de	PRAMC SGEN 0	
Main Config	guration Register	Main Confi	SIPI_0	
Address	0xFBE00000	Address	SIUL2 SMPU 0	
Size (bits)	32	Size (bits)	SPI_0 SPI_1	
			SPI_2	
	OK Cancel Help Apply		SRX_0 SRX_1	

## 3.2 MPC574x Automotive Math and Motor Control

All functions in the Automotive Math and Motor Control Functions Library v1.1.15 are supported as blocks for simulation and embedded target code generation for:

- Bit Accurate Model for 16-bit fixed-point implementation;
- Bit Accurate Model for 32-bit fixed-point implementation;
- Bit Accurate Model for floating-point single precision implementation;



The main functionalities supported are:

- Mathematical Function Library (MLIB) supports basic mathematical operations such as addition, multiplication, etc;
- General Function Library (GFLIB) supports basic trigonometric and general math functions such as sine, cosine, tan, hysteresis, limit, etc;
- General Digital Filters Library (GDFLIB) comprising digital IIR and FIR filters designed to be used in a motor control application;
- General Motor Control Library (GMCLIB) supports standard algorithms used for motor control such as Clarke/Park transformations, Space Vector Modulation, etc;
- Advanced Motor Control Function Library (AMCLIB) comprising advanced algorithms used for motor control purposes;

Check the  $\mbdtbx_mpc574x\AMMCLIB_s32k14x\AMMCLIB_MPC574xY\$ 

xxx\_AMMCLIB\_RTM\_1\_1\_15\_ReleaseNotes.txt for details about the latest changes

For each AMMCLIB block there is a Simulink Example available NXP Toolbox root directory ..\MPC\_Examples\common\ folder.



## 3.3 MPC574x Example Library

MPC574x Examples Library represents a collection of 102 Simulink models that let you test different MCU on-chip modules and run complex applications. The example library is split into:

- MPC574x Generic examples that can be run on any of the MPC574x Evaluation Boards
- **MPC574x Targeted** examples that are configured for a single target (e.g.: might contains peripherals that are available only on a specific target)



The examples are grouped in different layers that mimics a typical development flow: starting with basic building blocks that expose the MCU HW functionalities, build SIL and PIL models for verification and validation purposed and ending up with more complex applications that incorporates multiple building blocks.



The Simulink models shown as examples are enhanced with a comprehensive description to help users understand better the functionality that is exercised, hardware setup instructions whenever are necessary and a result validation section.

## 3.4 MATLAB Integration

The <u>NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0</u> is fully integrated with MathWorks environment.

The installation/uninstall actions are available via MATLAB Add-on Manager



The help & documentation for each of the Simulink block supported are available directly into MATLAB Help by pressing F1 or simply clicking on the Help button on each of the Simulink blocks.



## 4 New Model-Based Design Toolbox Extras

<u>NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0</u> enables additional functionalities that are not MCU specific but could help faster prototyping, validation and verification of the developed models.

## 4.1 New Code Profiling

NXP Model-Based Design Toolbox for MPC57xx provides blocks to be used for On-Target function profiling that returns results in units of clock cycles of execution per execution iteration. In 3.0.0 release you can enable MATLAB profile to measure the task and functions execution time when using Processor-in-the-Loop Mode

Solver Data Import/Export Math and Data Types Diagnostics Hardware Implementation Model Referencing Simulation Target	Code profilin	ng								
Solver Data Import/Export Math and Data Types Diagnostics Hardware Implementation Model Referencing Simulation Target	Code profili Measure f	ng								
Model Referencing Simulation Target	Workspac	are task execution time function execution time e variable:	s: Detailed (all functionexecutionProfile	on call sites)   ▼ Sa	ve options: All dat	a 🔻				
Simulation Target Code coverage for SIL or PIL										
Code Generation	Third-part	age for SIL or PIL			- Cont	figure				
Optimization Report	- Third-part	y tool. None			0011	igure				
Comments Symbols Custom Code Interface Code Style	<ul> <li>Enable p</li> <li>Enable s</li> </ul>	oortable word sizes ource-level debugging	for SIL							
Verification										
Templates Code Placement Data Type Replacement										
				OK Cance	el Help	App				
🎦 Profiling: sim_sil_top_mpc574x/Model — 🗆 🗙										
Profiling: sim_sil_top_m	pc574x/N	lodel			- 0	>				
Protiling: sim_sil_top_m	npc574x/№	lodel			- 0	>				
} Protiling: sim_sil_top_m ⇒ → ﷺ Block: Model	npc574x/№	lodel			- 0	> + [				
Protiling: sim_sil_top_m → → ↔ Block: Model Maximum Execution Time In ns	npc574x/M Average on Time in ns	lodel Maximum Self Time in ns	Average Self Time in ns	Calls		> • [				
Profiling: sim_sil_top_m → ☆ Slock: Model Maximum Execution Time In ns sim_sil_target_mpc574x_	Average Ion Time In ns	lodel Maximum Self Time in ns	Average Self Time in ns	Calls		> + [				
Profiling: sim_sil_top_m Profiling: sim_sil_top_m Maximum Execution Time In ns Sim_sil_target_mpc574x_ 37	Average on Time in ns _Start 37	Maximum Self Time in ns	Average Self Time in ns 37	Calls		> • (				
Profiling: sim_sil_top_m → ☆ Slock: Model Maximum Execution Time Execution in ns sim_sil_target_mpc574x_ sim_sil_target_mpc574x	Average on Time in ns _Start 37 [0.01 0]	Nodel Maximum Self Time in ns 37	Average Self Time in ns 37	Calls		> - [				

The reports are automatically displayed in MATLAB

Code Execution Profiling Report						_		×
The code execution profiling report provides metrics based on data collected from a SIL or PIL execution. Execution times are calculated from data recorded by instrumentation probes added to the SIL or PIL test harness or inside the code generated for each component. See Code Execution Profiling for more information. 1. Summary								
Total time		436125						
Unit of time		ns						
Command		report(ex 'Numeric	ecutionProfile, 'U Format', '%0.0f');	nits', 'seconds', 'S	caleFactor	', '1e	-09',	
Timer frequency (ticks per second)		2.701e+0	9					
Profiling data created		16-Feb-2	019 16:05:58					
2. Profiled Sections of Code	Maximum Execution Time in ns	Average Execution Time in ns	Maximum Self Time in ns	Average Self Time in ns	Calls			
<pre>sim_sil_target_mpc574x_Start</pre>	37	37	37	37	1	Ξċ	4 🗄	<b>V</b>
[-] <u>sim_sil_target_mpc574x [0.01 0]</u>	18197	436	17786	80	1001	Ξċ	4 🗄	<b>V</b>
FlexPWM_Config	7806	28	7806	28	1001	Ξċ	4 🗄	<b>V</b>
<u>GFLIB_Sin_SF</u>	17118	327	17118	327	1001	÷ċ	1	<b>v</b>
					C	ЭK		Help

## 4.2 Processor-In-the-Loop Support (PIL)

The NXP Model-Based Design Toolbox for MPC574x provides PIL support for purposes of ASIL software development processes, "Model PIL Block" (Model Reference) and "PIL Block" modes of operation are supported "Top Model PIL" mode is not supported. PIL contains full support for Math and Motor Control Blocks, and limited support for peripheral blocks. No support for interrupts exist in supported PIL modes of execution, therefore no blocks with interrupts are supported in PIL mode.

For PIL support you need to install the Microsoft Windows SDK 7.1. Please refer to the following links for troubleshooting:

- https://www.mathworks.com/matlabcentral/answers/95039-why-does-the-sdk-7-1installation-fail-with-an-installation-failed-message-on-my-windows-system
- https://www.mathworks.com/matlabcentral/answers/101105-how-do-i-install-microsoftwindows-sdk-7-1

An alternative to Microsoft Windows SDK is the <u>MATLAB Support for MinGW-w64 C/C++</u> Compiler toolbox

#### 4.3 Boot Loader

Internal Boot Loader is a standalone application which requires the Microsoft .NET Framework version 4.0 installed on PC. It may be required to download the package from http://www.microsoft.com/download and install if you are going to use PIL and internal Boot Loader. The board should be configured to work with BAM to use Internal Boot Loader. Please check board documentation.

The bootloader files have need updated to support all MPC574x derivatives and allows applications to be downloaded via UART or CAN interfaces to the boards.

# **5** Prerequisites

## 5.1 MATLAB Releases and OSes Supported

This toolbox is developed and tested to supports the following MATLAB releases:

- R2017A;
- R2017B;
- R2018A;
- R2018B;

In general, older version of MATLAB may be supported with small updates that can be done after installation. As a rule, if you are planning to use an older version you need to update the Simulink version in every MDL file delivered with the toolbox

mbo	d_mpc574x_ec_toolbox.mdl 🛛 💥	+
1	Library {	
2	Name "m	bd_mpc574x_ec_toolbox"
3	Version 8.	9
4	SavedCharacterEnco	ding "windows-1252"
5	LogicAnalyzerGraph	icalSettings ""
6	LogicAnalyzerPlugi	n "on"
7	LogicAnalyzerSigna	lOrdering ""
8	DiagnosticSuppress	or "on"
9	SuppressorTable	"22 serialization::archive 11 0 6 0 0 0 8 0"
10	SLCCPlugin	"on"
11	LibraryType	"BlockLibrary"
12	ScopeRefreshTime	0.035000
13	OverrideScopeRefre	shTime off
14	DisableAllScopes	off

For a flowless development experience the minimum recommended PC platform is:

- *Windows* ® *OS*: any x64 processor
- At least 4 GB of RAM
- At least 6 GB of free disk space.
- Internet connectivity for web downloads.

#### **Operating System Supported**

	SP Level	64-bit
Windows 7	SP1	Х
Windows 10		Х

#### 5.2 Compiler Support

The following compilers are the NXP official build tools versions supported by the SDK and AMMCLIB Software Libraries & Driver which are a pre-requisite of the NXP Model-Based Design Toolbox:

Compiler Supported	Release Version
GCC E200 VLE GNU Compiler	V4.9.4
GreenHills MULTI 7.1.4 Compiler	2015.1.6
WindRiver DIAB Compiler	5.9.6.2

Any other version might produce wrong results or might fails to compile due to missing dependencies.

The target compiler for Model-Based Design Toolbox needs to be configured. Use the notation below to setup these compiler environmental variables. Ensure a system environment variable called <COMPILER\_STRING>\_TOOL, corresponding to the compiler(s) you have installed, is defined to compiler path value as shown below:

Note: Paths shown are for illustration, your installation path may be different. Once environmental variables are setup you will need to restart MATLAB for the IDE environment to see these system variables.

In case there is no compiler installed, the NXP's Model-Based Design Toolbox is going to default to the internal GCC 4.9.4 compiler.



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# 6 Known Limitations

NXP's Model-Based Design Toolbox for MPC57xx version 3.0.0 has the following limitations:

- The version 3.0.0 cannot coexist with previously installed version of the NXP Model-Based Design Toolbox for MPC57xx due to MATLAB global paths limitations. When the toolbox is install as Add-Ons the MATLAB will uninstall the previous version automatically. In case you have files, custom files saved with previous versions those will not be deleted.

To avoid any complications, it is recommended to uninstall the previous toolboxes for MPC57xx manually prior of install the 3.0.0 release.

- The Simulink Function-Call Split block cannot be mixed with Model-Based Design Toolbox blocks.
- Download to target for Simulink is supported only via UART interface. For CAN download, you can use the RAppID Bootloader as a separate tool to load the generated application into target RAM/Flash Memories.
- In case of migrating a project from an earlier toolbox version to the latest 3.0.0 the user needs to manually check each Simulink model and relink the blocks to new Simulink Library available.

# 7 Support Information

For technical support please sign on to the following NXP's Model-Based Design Toolbox Community: <u>https://community.nxp.com/community/mbdt</u>

How to Reach Us:

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