Mask Set Errata

MSE908KX8_2L09H
4/2004

Mask Set Errata for 908KX8, Mask 2L09H

Freescale Semiconductor, Inc.

Introduction

This mask set errata applies to this 908KX8 MCU mask set:

- 2L09H

MCU Device Mask Set Identification

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 2L09H. All standard devices are marked with a mask set number and a date code.

MCU Device Date Codes

Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code “0201” indicates the first week of the year 2002.

MCU Device Part Number Prefixes

Some MCU samples and devices are marked with an SC, PC, or XC prefix. An SC prefix denotes special/custom device. A PC prefix indicates a prototype device which has undergone basic testing only. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC or SC prefix.

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ERASE Routine in ROM

SE23-ERASE

The ROM-based erase routine, called ERARNGE and located at $1006 on this device, may cause the erase of the vector page when called to erase another page of FLASH. This behavior is a side-effect of servicing the COP during this routine's necessary delay of 1 ms to 4 ms.

Workarounds:

- Set block protection so that at least the vector page is protected from erase and inadvertent reprogramming. Then any page erases, other than an attempt to erase the vector page, will result in the intended page being erased, but the vector page will remain intact.
- Accept the erase of the vector page by first buffering the data of this page in RAM and then, after the intended page is erased along with the vector page, reprogram the vector page with the buffered data.

Do not use the erase routine in ROM. Write your own erase routine and keep it in FLASH. Whenever a page erase is desired, copy the routine to RAM and execute the erase from there. For help on writing the erase routine, consult the MC68HC908KX8/MC68HC908KX2 Advance Information, Motorola document order number MC68HC908KX8/D, or the application note Using MC68HC908 On-Chip FLASH Programming Routines, Motorola document order number AN1831/D. If using the routine from AN1831, which is the routine stored in ROM, make sure to delete the instructions that service the COP.

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