

## Mask Set Errata

MSE908KX2\_0K45D  
12/2002

Mask Set Errata for 908KX2,  
Mask 0K45D



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## Introduction

This mask set errata applies to this 908KX2 MCU mask set:

- 0K45D

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## MCU Device Mask Set Identification

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 0K45D. All standard devices are marked with a mask set number and a date code.

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## MCU Device Date Codes

Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "0201" indicates the first week of the year 2002.

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## MCU Device Part Number Prefixes

Some MCU samples and devices are marked with an SC, PC, or XC prefix. An SC prefix denotes special/custom device. A PC prefix indicates a prototype device which has undergone basic testing only. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC or SC prefix.

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**Integrated Clock Generator (ICG)**

SE24-ICG

The low-frequency base clock (Ibase) is approximately half of its intended 307.2 kHz. Larger values of N (approximately 2x) can be used to compensate for this, up to the maximum attainable frequency of  $\sim 150 \text{ kHz} \times 127 = \sim 19 \text{ MHz}$ , or about a 4.8 MHz bug.

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**Analog-to-Digital Converter (ADC)**

SE25-ADC

Absolute ADC accuracy is approximately two least significant bits (LSB) in the worst case rather than the specified 1.5 LSB.



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