Mask Set Errata for Mask 3M73B

Introduction

This mask set errata applies to the mask 3M73B for these products:

- MC68HC908EY16A
- MC68HC908EY8A

MCU Device Mask Set Identification

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 3M73B. All standard devices are marked with a mask set number and a date code.

Incorrect TBM Interrupt Rates   SE109-TBM

Description

When used with the divide-by-128 prescaler enabled (TMBCLKSEL = 1), the three longest TBM interrupt periods (TBR = 0, 1, 2) will give incorrect and inconsistent interrupt periods. The exact interrupt period for a given frequency can not be predicted and these combinations of register settings must not be used.

Workaround

If the TBM interrupts are only used when the system is not in stop or wait mode, then a software counter can be used to recreate the divide-by-128 prescaler. Using this technique in stop or wait raises the average supply current.