

# LPC804

## Errata sheet LPC804

Rev. 1.3 — 12 December 2019

Errata sheet

### Document information

Info	Content
<b>Keywords</b>	LPC804M101JDH20;LPC804M101JDH24;LPC804M111JDH24;LPC804M101JHI33;804UK; LPC804 errata
<b>Abstract</b>	<p>This errata sheet describes both the known functional problems and any deviations from the electrical specifications known at the release date of this document.</p> <p>Each deviation is assigned a number and its history is tracked in a table.</p>



## Revision history

Rev	Date	Description
1.3	20191211	<ul style="list-style-type: none"><li>Added device revision (1C) and updated VDD.1 errata.</li></ul>
1.2	20180712	<ul style="list-style-type: none"><li>Added LPC804UK device.</li></ul>
1.1	20180427	<ul style="list-style-type: none"><li>Updated <a href="#">Table 1 “Device revision table”</a>.</li><li>Updated work-around for <a href="#">Section 3.1 “DPD.1: Port pin PIO0_4 cannot be used to wake up the device from deep power-down mode.”</a>.</li></ul>
1.0	20171214	Initial version.

## Contact information

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## 1. Product identification

The LPC804 HVQFN33 packages have the following top-side marking::

- First line: LPC804M1
- Second line: xxxx
- Third line: yywwx[R]
  - yyww: Date code with yy = year and ww = week.
  - xR = Boot code version and device revision.

The LPC804 TSSOP20 packages typically have the following top-side marking:

- First line: LPC804
- Second line: M101
- Third line: xxxx
- Fourth line: xxywwx[R]
  - yyww: Date code with y = year and ww = week.
  - xR = Boot code version and device revision.

The LPC804 TSSOP24 packages have the following top-side marking:

- First line: LPC804
- Second line: xxxx
- Third line: ywwx[R]
  - yyww: Date code with y = year and ww = week.
  - xR = Boot code version and device revision.
- Fourth line: M1y1J
  - y: 0 or 1

The LPC804 WLCSP20 packages have the following top-side marking:

- First line: LPC804
- Second line: xxxxx
- Third line: xyywwx[R]
  - yyww: Date code with ww = week and yy = year.
  - xR = Boot code version and device revision.
- Fourth line: xxx - yyy

**Table 1. Device revision table**

Revision identifier (R)	Revision description
1A	Initial device revision with Boot ROM version 13.1
1B	Initial device revision with Boot ROM version 13.1
1C	Initial device revision with Boot ROM version 13.1

## 2. Errata overview

Table 2. Functional problems table

Functional problems	Short description	Revision identifier	Detailed description
DPD.1	Port pin PIO0_4 cannot be used to wake up the device from deep power-down mode.	1A	<a href="#">Section 3.1</a>

Table 3. AC/DC deviations table

AC/DC deviations	Short description	Revision identifier	Detailed description
VDD.1	The minimum rise time of the power supply ramp must be 1 ms or less.	1A, 1B	<a href="#">Section 4.1</a>

Table 4. Errata notes

Note	Short description	Detailed description
n/a	n/a	n/a

### 3. Functional problems detail

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#### 3.1 DPD.1: Port pin PIO0\_4 cannot be used to wake up the device from deep power-down mode.

**Introduction:**

On the LPC804, the following pins can be used as WAKEUP pins to wake up from deep power-down mode: PIO0\_4, PIO0\_8, PIO0\_9, PIO0\_10, PIO0\_11, PIO0\_13, PIO0\_15, and PIO0\_17.

**Problem:**

The PIO0\_4 pin does not function properly and cannot be used to wake up the device from deep power-down mode. Other pins listed above are functional.

**Work-around:**

This issue is fixed in device revision 1B.

### 4. AC/DC Deviation

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#### 4.1 VDD.1: The rise time of the power supply ramp must be 1 ms or below.

**Problem:**

The LPC804 device might not always start-up if the rise time of the power supply ramp on the VDD pin is 1 ms or above.

**Work-around:**

For device revisions 1A, and 1B, the rise time of power supply ramp must be 1 ms or below. This issue is fixed in device revision 1C.

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Date of release: 12 December 2019

Document identifier: LPC804

