



October 3, 2006

CSM12D

User Guide Correction

Versions of the CSM12D User Guide (UG) dated before September 11, 2006 contain an error. The signal assignment for connector J1 shown in the document is incorrect. In the early revisions, the PortA and PortB signal assignment is reversed.

The signal assignment at Connector J1 has been corrected and updated documents can be retrieved from either the Axiom Manufacturing or Freescale web sites. All versions of the board schematic show the correct signal assignment at connector J1.

We sincerely regret any inconvenience this UG error may have caused.

Design and/or Manufacturing services for this product provided by:

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