QorIQ® P3041 Quad-Core Communications Processors

P3041

新規採用非推奨
このページでは、新規設計を推奨しない製品に関する情報を掲載しています。

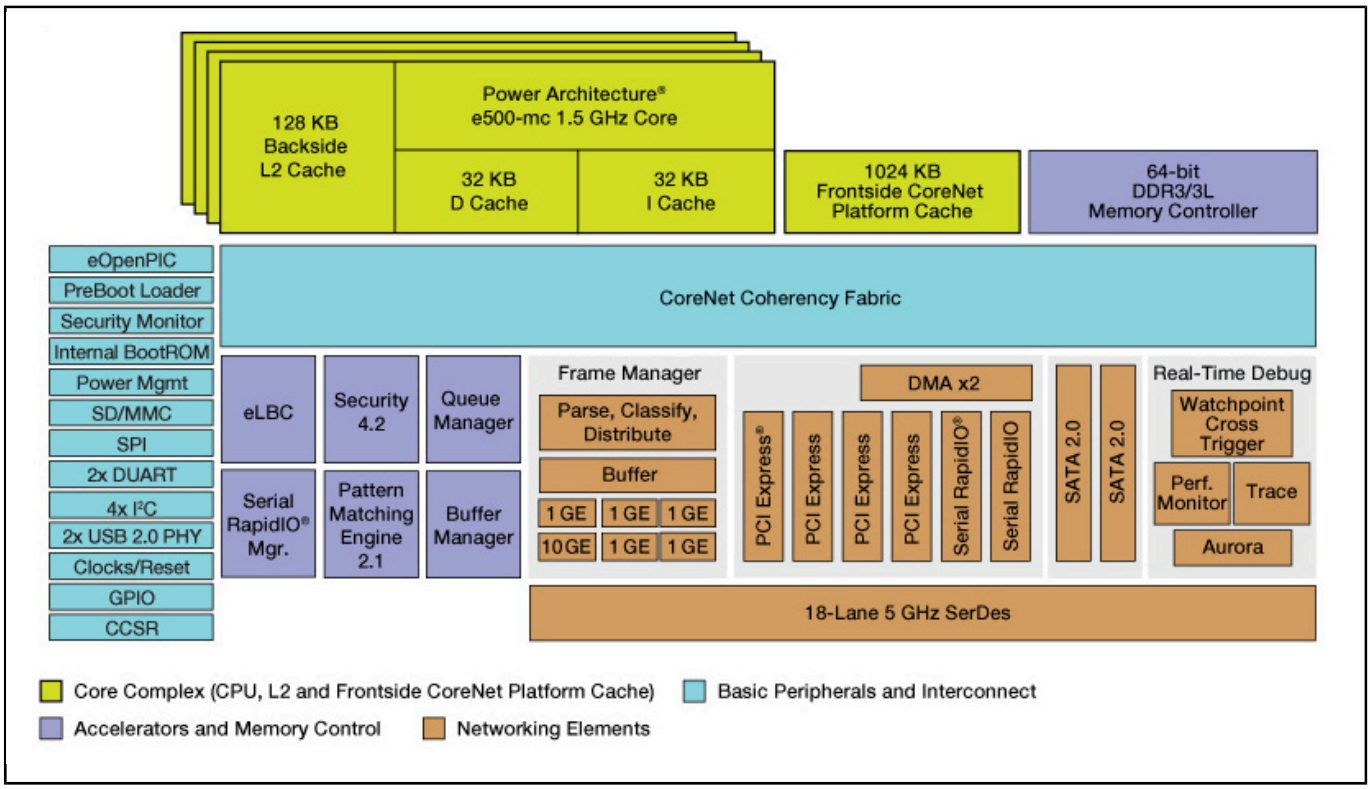
Last Updated: Oct 17, 2022

P3041 device is "Not recommended for new designs", please use the replacement families Power Architecture (T208x), Arm Architecture (LS1046, LS2080).

The QorIQ® P3041 processor is an optimized quad-core device that leverages architectural features pioneered in the P4 platform. Built on Power Architecture® technology, the P3041 fits into many of the same applications as the P4 platform processors, yet is designed to offer a more power- and cost-efficient solution.

The P3041 includes P4 platform features such as the three-level cache hierarchy for low latencies, hardware hypervisor for robust virtualization support, data path acceleration architecture (DPAA) for offloading packet handling tasks from the core and the CoreNet® switch fabric which eliminates internal bottlenecks. This enables architectural compatibility from the P3041 to the P4 platform and also to the P5 platform, which uses the same architecture. P3041 is pin-compatible with P4040, P4080, P5010, and P5020.

P3041 BDIMG Block Diagram



View additional information for QorIQ® P3041 Quad-Core Communications Processors.

Note: The information on this document is subject to change without notice.