



Applications Processors – Low-Power, Hardware-Accelerated Video, High-Performance, Arm® Cortex®-A8 Core

i.MX513

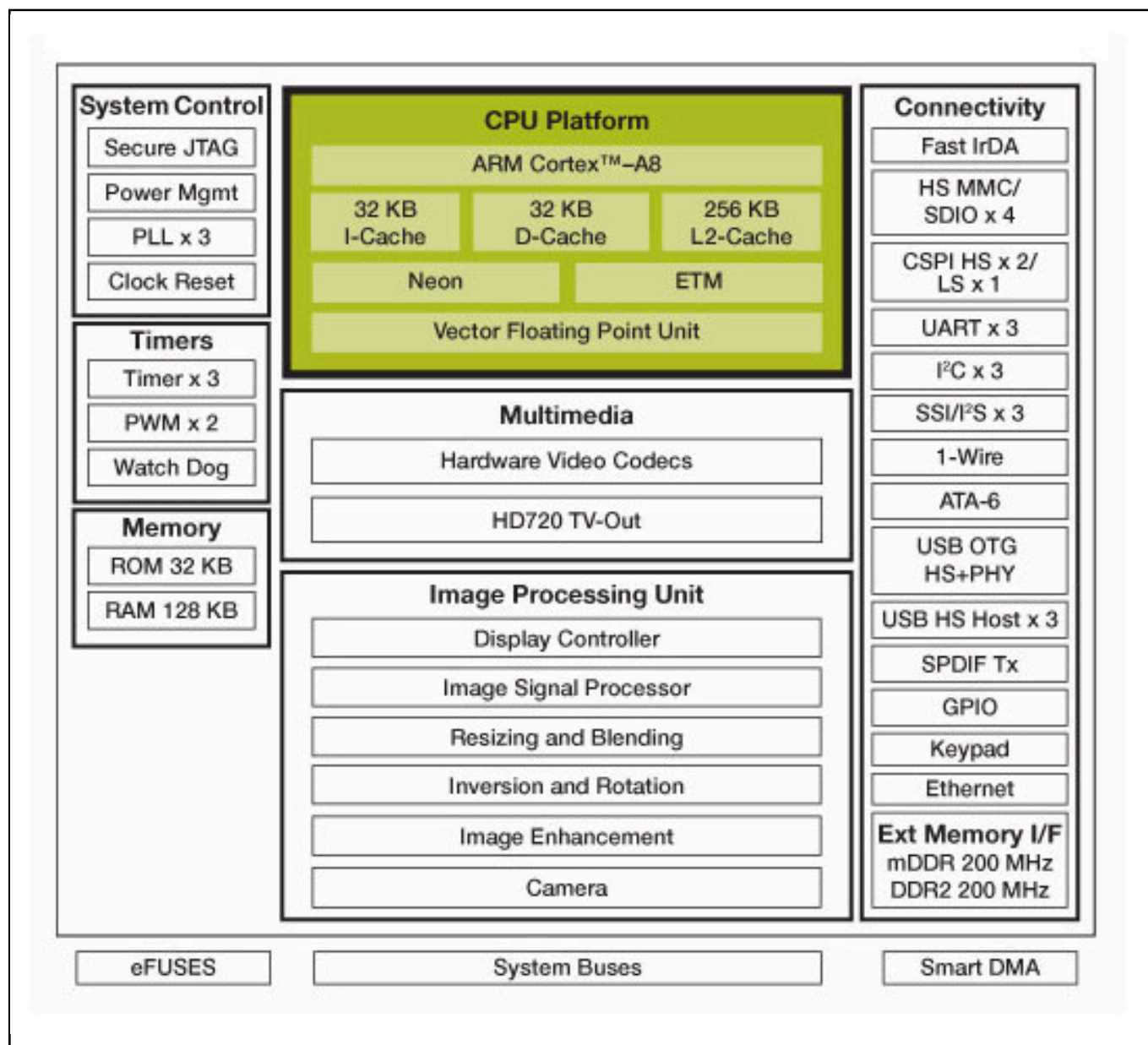
Last Updated: Apr 10, 2024

The i.MX513 multimedia applications processor offers high performance processing optimized for the lowest power consumption and hardware accelerated video codecs for IP cameras, V2IP media phones, digital signage and HMI. It features Our advanced and power-efficient implementation of the Arm® Cortex®-A8 core, which operates at speeds up to 800 MHz. Extended temperature is available for industrial focused devices running at up to 600 MHz.

The i.MX513 is supported by companion NXP® power management ICs (PMIC), [MC13892](#), [MC34704](#), [MC34709](#) and [MMPF0100](#).

For consumer and industrial i.MX51 versions, i.MX515 samples are orderable.

i.MX513 Applications Processor Block Diagram Block Diagram



View additional information for [Applications Processors - Low-Power, Hardware-Accelerated Video, High-Performance, Arm® Cortex®-A8 Core](#).

Note: The information on this document is subject to change without notice.