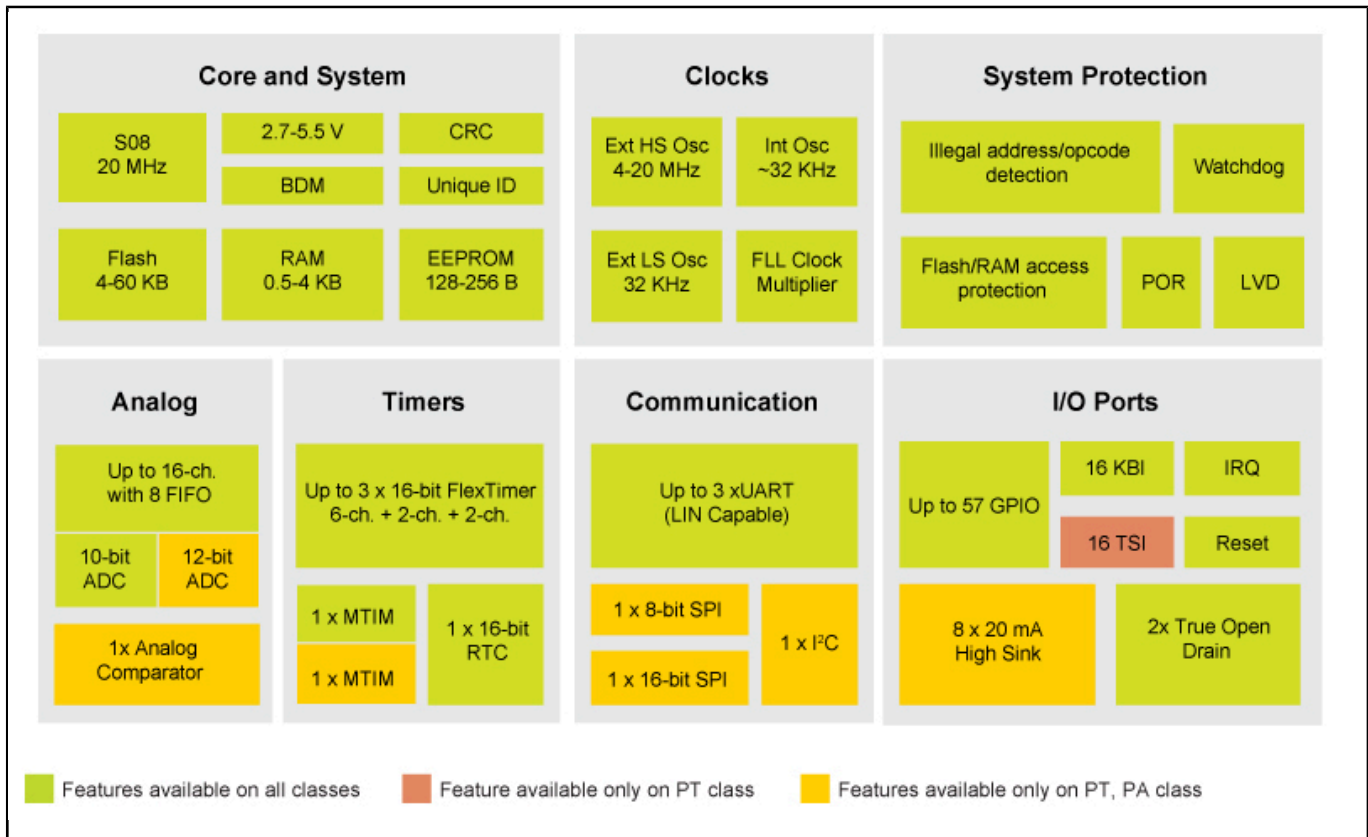# S08P–Lite Evaluation Board

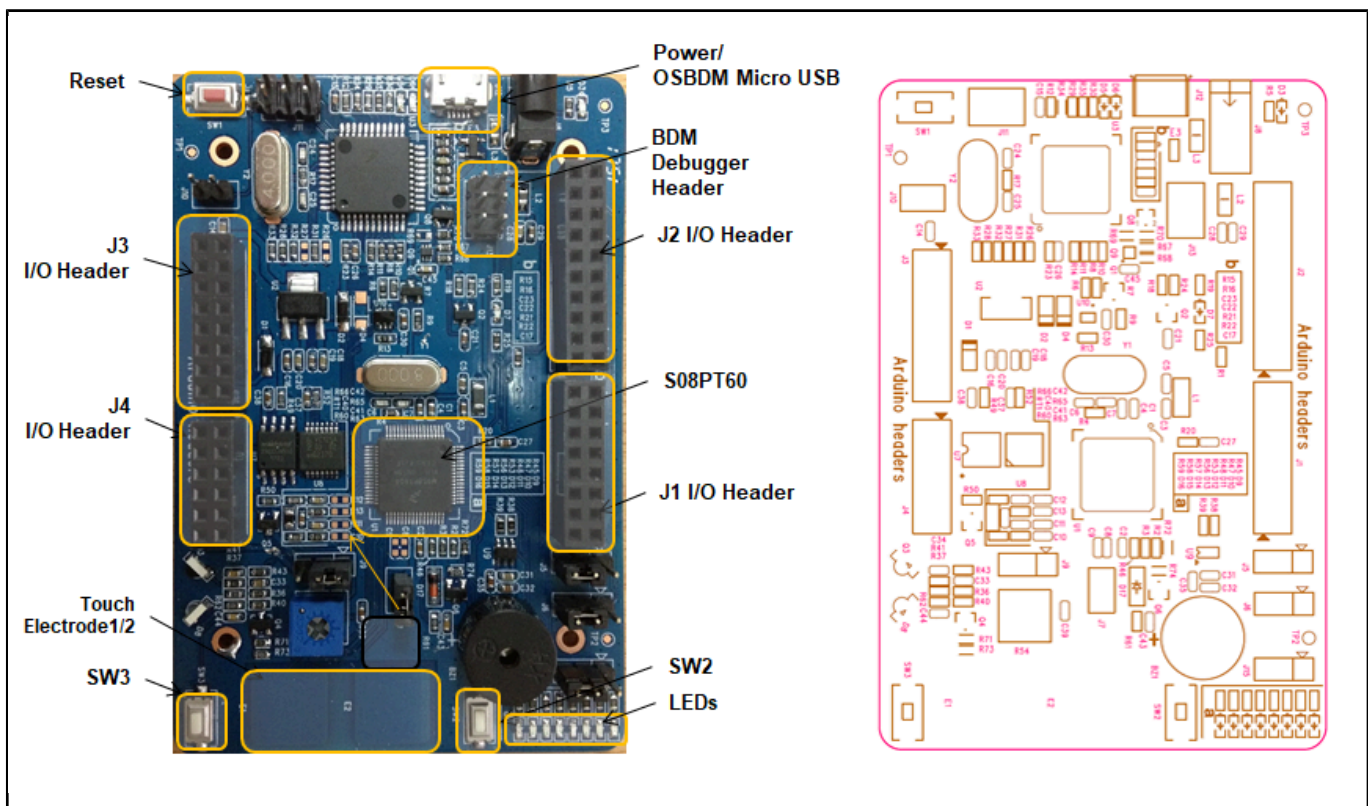## S08P-LITE

Last Updated: Dec 1, 2022

The S08P-Lite is a cost-effective development hardware for the NXP S08P rugged 5V 8-bit MCUs. It's small, yet powerful, with rich integrated peripherals to evaluate all features of S08P MCUs. S08P-Lite support OSBDM debugs, so it can be used to debug without an external debugger tool. Sample code and schematic are free to download for users' reference.

Click to Buy

MC9S08P Block Diagram Block Diagram

Core and System

S08 20 MHz	2.7-5.5 V	CRC
	BDM	Unique ID
Flash 4-60 KB	RAM 0.5-4 KB	EEPROM 128-256 B

Clocks

Ext HS Osc 4-20 MHz	Int Osc ~32 KHz
Ext LS Osc 32 KHz	FLL Clock Multiplier

System Protection

Illegal address/opcode detection	Watchdog
Flash/RAM access protection	POR LVD

Analog

- Up to 16-ch. with 8 FIFO
- 10-bit ADC
- 12-bit ADC
- 1x Analog Comparator

Timers

- Up to 3 x 16-bit FlexTimer 6-ch. + 2-ch. + 2-ch.
- 1 x MTIM
- 1 x 16-bit RTC
- 1 x MTIM

Communication

- Up to 3 xUART (LIN Capable)
- 1 x 8-bit SPI
- 1 x I²C
- 1 x 16-bit SPI

I/O Ports

Up to 57 GPIO	16 KBI IRQ
	16 TSI Reset
8 x 20 mA High Sink	2x True Open Drain

- Features available on all classes
- Feature available only on PT class
- Features available only on PT, PA class

Get to know the S08P-Lite board Block Diagram

Reset
Power/ OSBDM Micro USB
BDM Debugger Header
J2 I/O Header
J3 I/O Header
J4 I/O Header
S08PT60
J1 I/O Header
Touch Electrode1/2
SW3
SW2
LEDs

Arduino headers

View additional information for S08P-Lite Evaluation Board.

Note: The information on this document is subject to change without notice.