



# Universal LCD Driver for Low Multiplex Rates

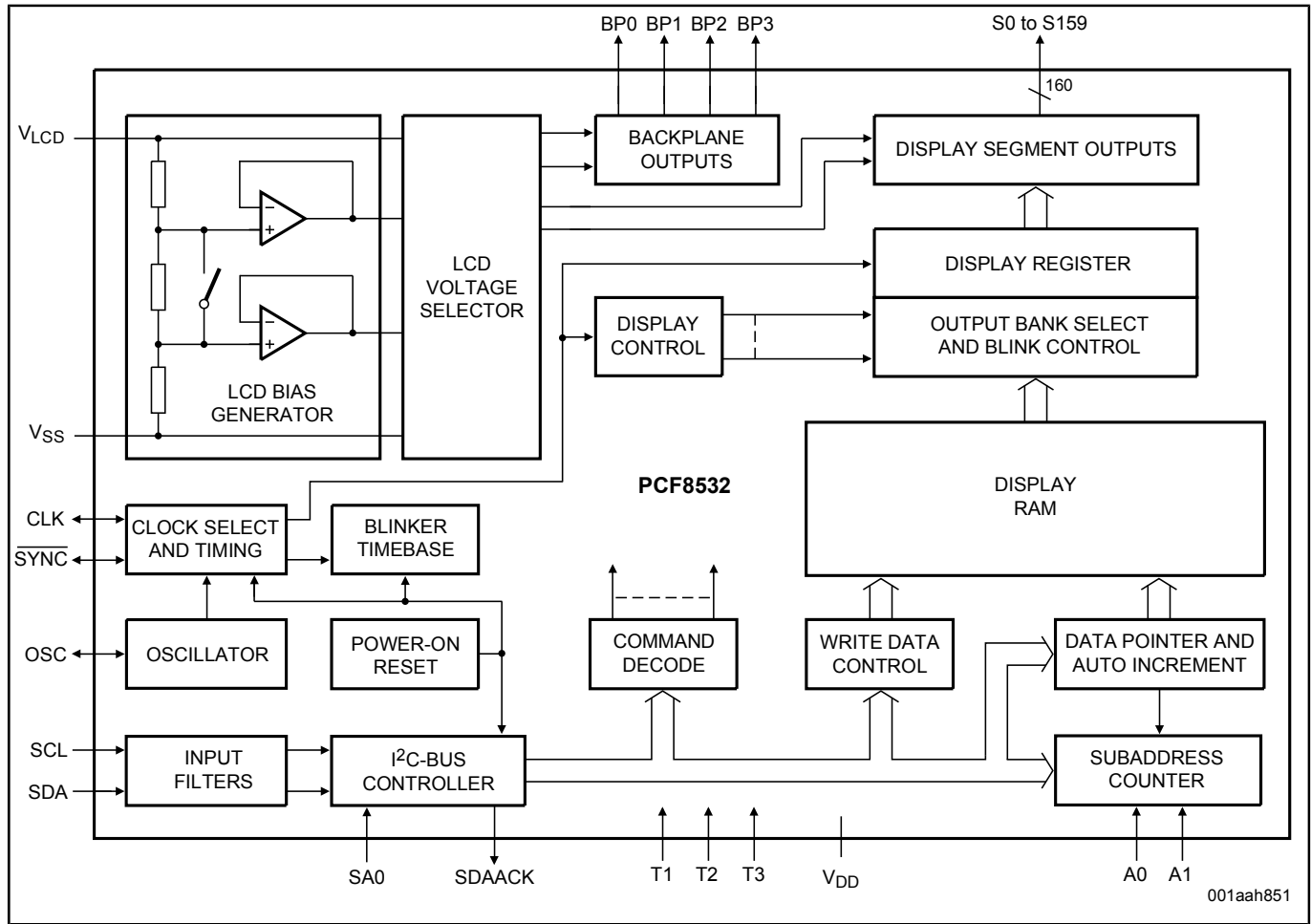
## PCF8532U

Last Updated: Oct 13, 2023

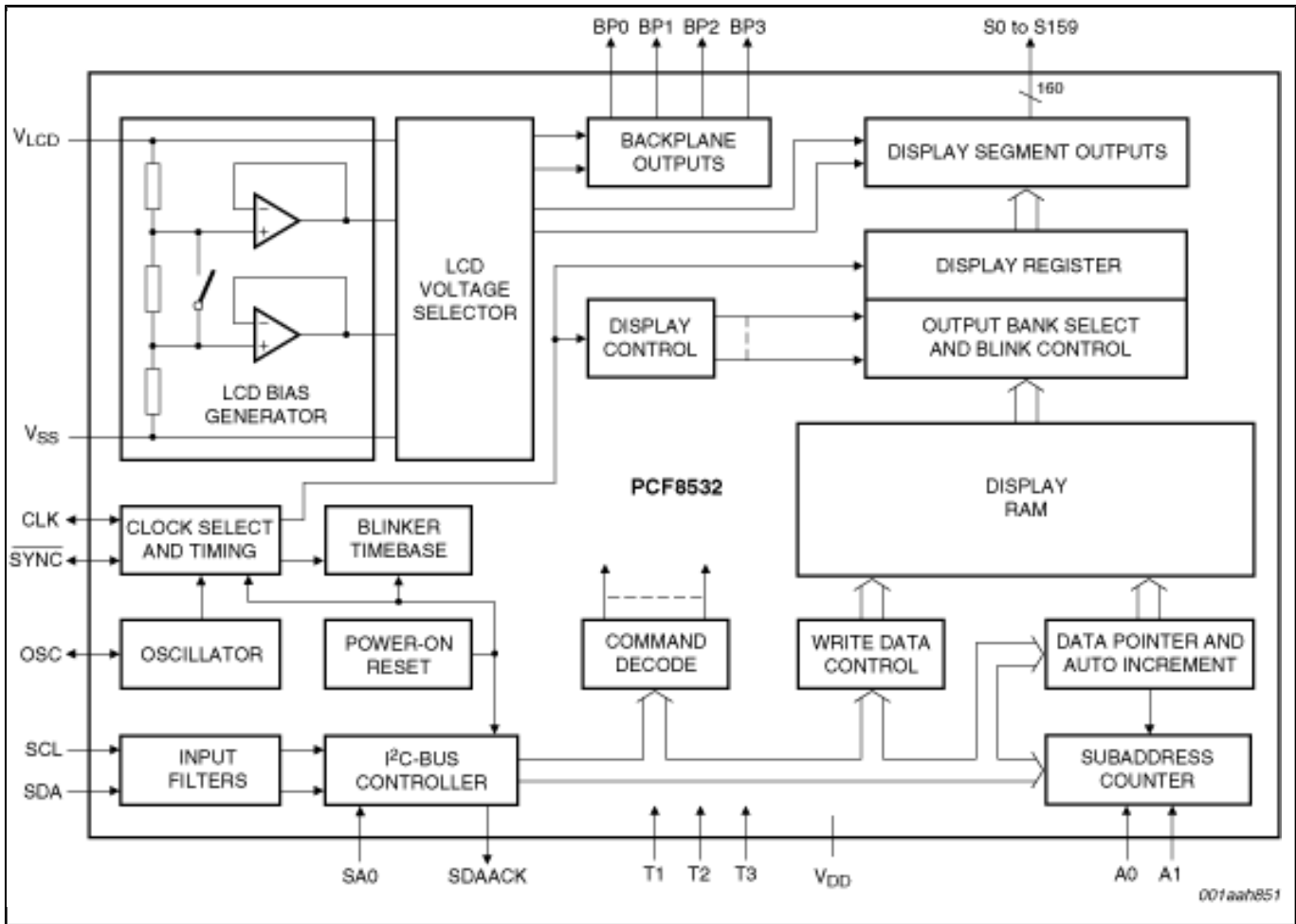
The PCF8532 is a peripheral device which interfaces to almost any Liquid Crystal Display (LCD) with low multiplex rates. It generates the drive signals for any static or multiplexed LCD containing up to four backplanes and up to 160 segments. It can easily be cascaded for larger LCD applications. The PCF8532 is compatible with most microcontrollers and communicates via the two-line bidirectional I<sup>2</sup>C-bus. Communication overheads are minimized by a display RAM with auto-incremental addressing, by hardware subaddressing, and by display memory switching (static and duplex drive modes).

Not to be used for new designs. Replacement part is PCF85132U/2DA/1 for industrial and PCA85132U/2DA/Q1 for automotive applications.

# PCF8532 Block Diagram Block Diagram



# Block diagram: PCF8532U Block Diagram



View additional information for [Universal LCD Driver for Low Multiplex Rates](#).

Note: The information on this document is subject to change without notice.

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