



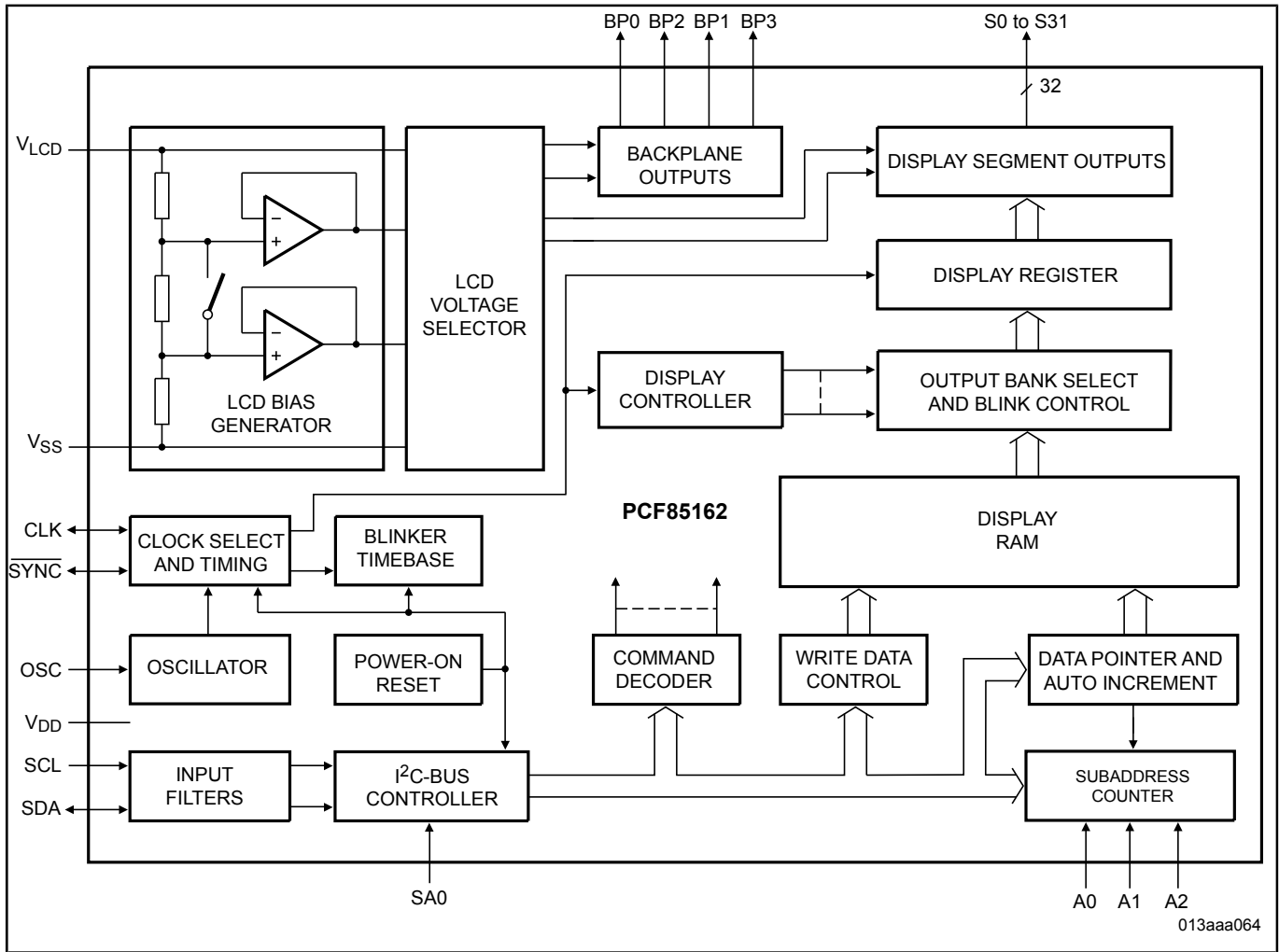
32 × 4 Universal LCD Driver for Low Multiplex Rates

PCF85162T

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The PCF85162 is a peripheral device which interfaces to almost any Liquid Crystal Display (LCD) with low multiplex rates. It generates the drive signals for any static or multiplexed LCD containing up to four backplanes and up to 32 segments. It can be easily cascaded for larger LCD applications. The PCF85162 is compatible with most microcontrollers and communicates via the two-line bidirectional I²C-bus. Communication overheads are minimized by a display RAM with auto-incremented addressing, by hardware subaddressing, and by display memory switching (static and duplex drive modes).

PCF85162 Block Diagram Block Diagram



View additional information for [32 × 4 Universal LCD Driver for Low Multiplex Rates](#).

Note: The information on this document is subject to change without notice.

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