



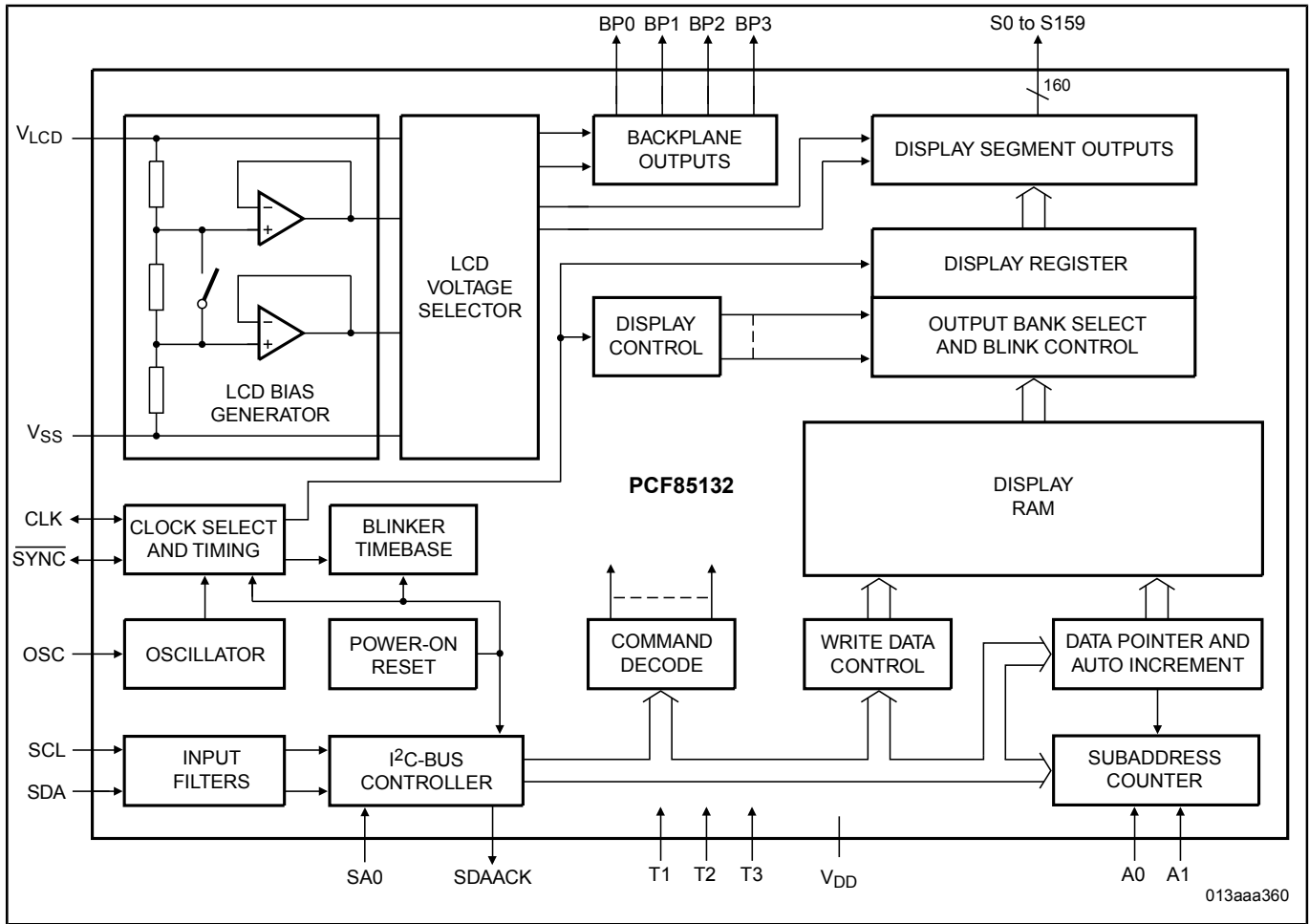
# LCD Driver for Low Multiplex Rates

## PCF85132U

Last Updated: Oct 13, 2023

The PCF85132 is a peripheral device which interfaces to almost any Liquid Crystal Display (LCD) with low multiplex rates. It generates the drive signals for any static or multiplexed LCD containing up to four backplanes and up to 160 segments. It can be easily cascaded for larger LCD applications. The PCF85132 is compatible with most microprocessors or microcontrollers and communicates via a two-line bidirectional I<sup>2</sup>C-bus. Communication overheads are minimized by a display RAM with auto-incremental addressing, by hardware subaddressing, and by display memory switching (static and duplex drive modes).

# PCF85132 Block Diagram Block Diagram



View additional information for [LCD Driver for Low Multiplex Rates](#).

Note: The information on this document is subject to change without notice.

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