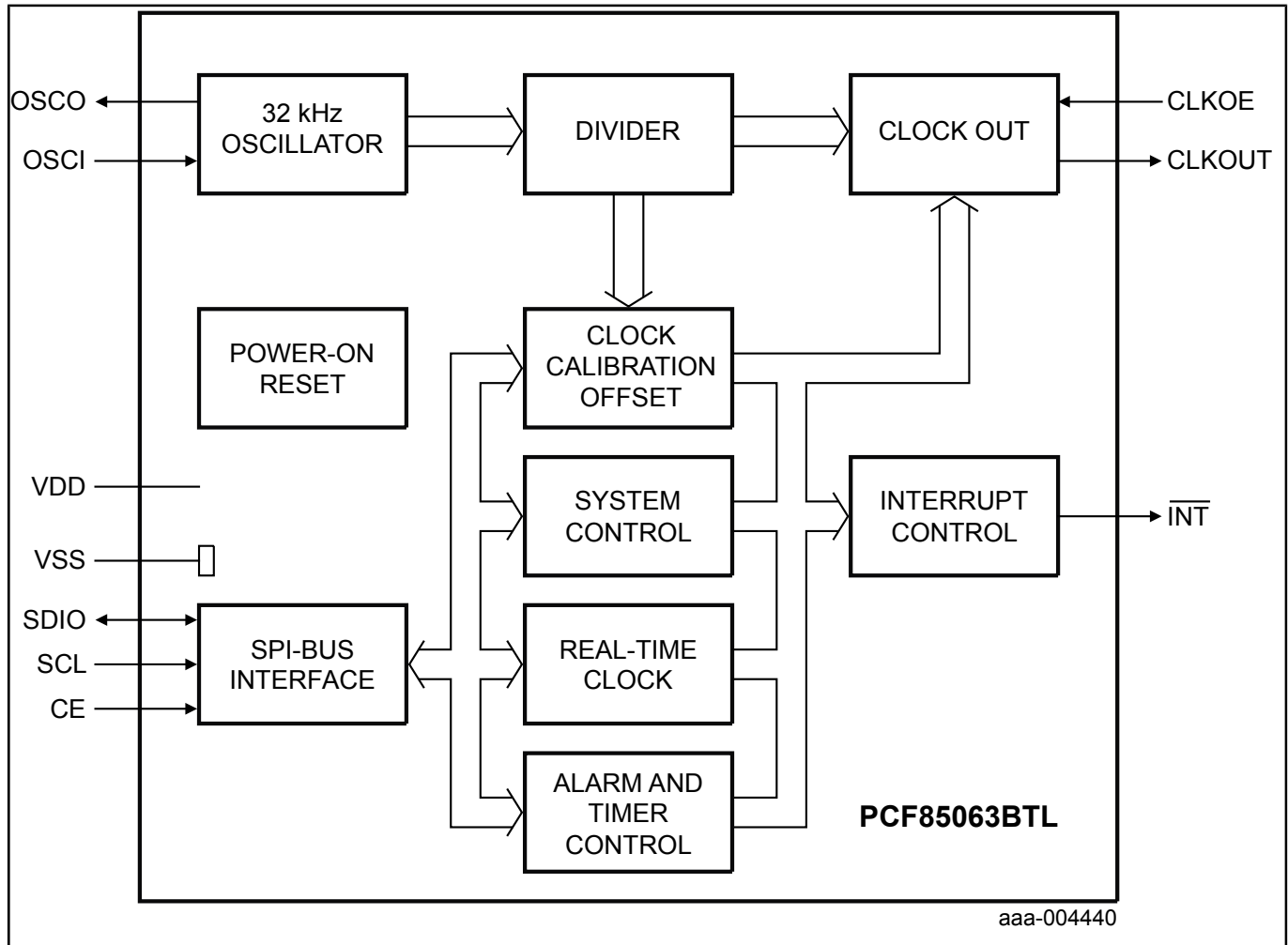# Tiny Real-Time Clock/Calendar with Alarm Function and SPI-Bus

PCF85063B

Last Updated: Apr 11, 2024

The PCF85063BTL is a CMOS Real-Time Clock (RTC) and calendar optimized for low power consumption. An offset register allows fine-tuning of the clock. All addresses and data are transferred serially via a Serial Peripheral Interface (SPI#bus) with a maximum data rate of 7 Mbit/s. The register address is incremented automatically after each written or read data byte.

PCF85063BTL Block Diagram Block Diagram

OSCO ← 32 kHz OSCILLATOR → DIVIDER → CLOCK OUT	CLKOE, CLKOUT

OSCO ◄—— 32 kHz OSCILLATOR

OSCI ——►

DIVIDER

CLOCK OUT ◄—— CLKOE

CLKOUT ——►

POWER-ON RESET

CLOCK CALIBRATION OFFSET

VDD

VSS

SYSTEM CONTROL

INTERRUPT CONTROL ——► $\overline{INT}$

SDIO ◄——

SCL ——►

CE ——►

SPI-BUS INTERFACE

REAL-TIME CLOCK

ALARM AND TIMER CONTROL

PCF85063BTL

aaa-004440

View additional information for Tiny Real-Time Clock/Calendar with Alarm Function and SPI-Bus.

Note: The information on this document is subject to change without notice.