32 Bit Microcontroller

MPC566
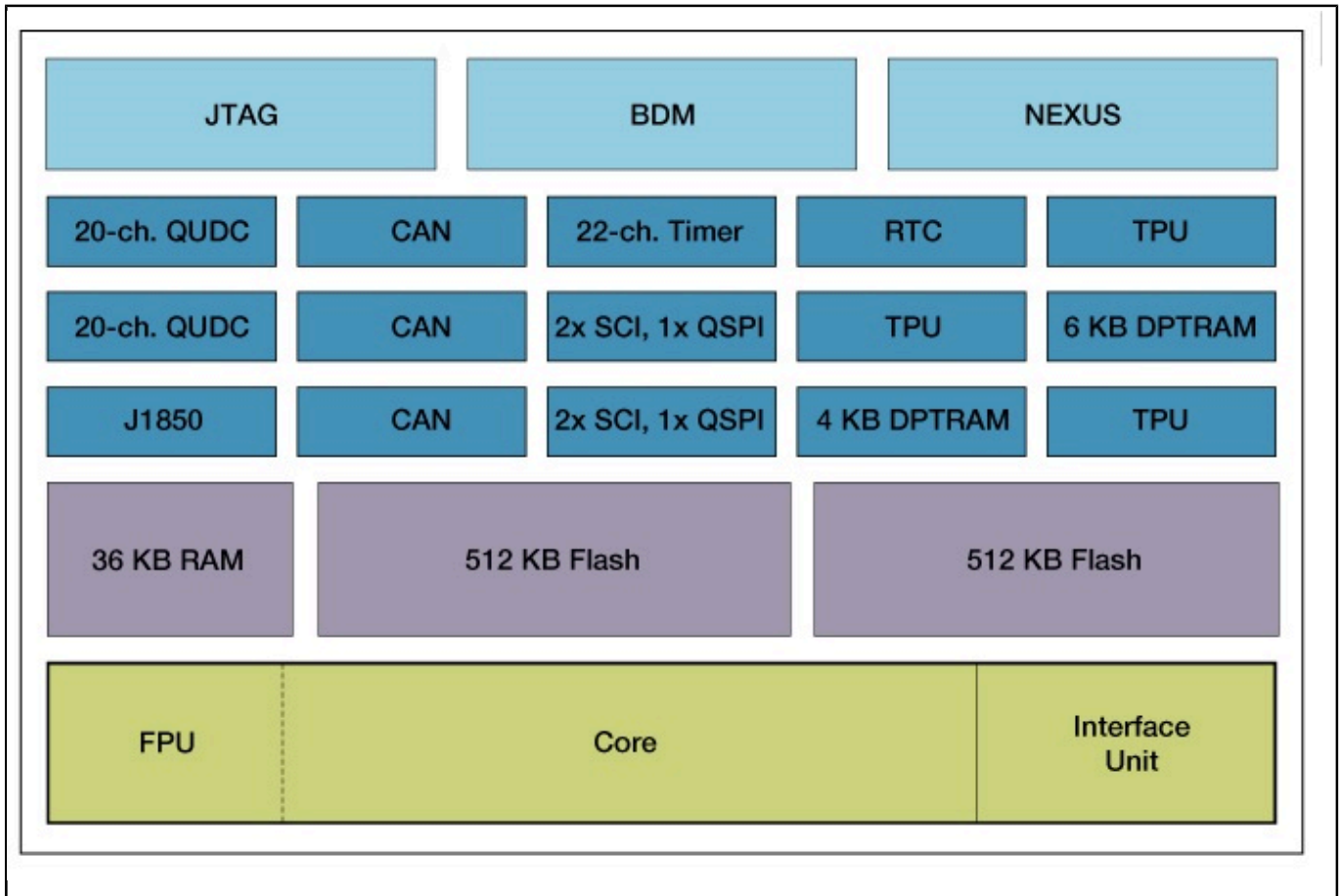
Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

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The MPC566 integrates a Power Architecture® core with a dual precision floating point unit and BCC, 36 KB of RAM and 1 MB of flash memory.

The MPC566, an upgraded version of the MPC565, offers code compression to enable more efficient use of internal or external flash memory. Code compression is optimized for automotive (non-cached) applications and the new scheme increases performance by 40 to 50 percent.

MPC566 Block Diagram Block Diagram

JTAG	BDM	NEXUS

20-ch. QUDC	CAN	22-ch. Timer	RTC	TPU
20-ch. QUDC	CAN	2x SCI, 1x QSPI	TPU	6 KB DPTRAM
J1850	CAN	2x SCI, 1x QSPI	4 KB DPTRAM	TPU

36 KB RAM	512 KB Flash	512 KB Flash

FPU	Core	Interface Unit

View additional information for 32 Bit Microcontroller.

Note: The information on this document is subject to change without notice.