Table of Contents

Revisions

Rev	Description	Date
X1	Original Release	07-19-15
A	Release	11-3-15
A1	Update (1x3 header : 210-80734)	12-18-15
A2	Freescale LOGO	1-12-15

FRDMSTBC-P3115

RF, Analog & Sensor Group

6501 William Cannon Drive West
Austin, TX 78735-8598

ICAP Classification: FCP: FIUO: X   PUBI:

Designer: Frank L	Drawing Title: **FRDMSTBC-P3115**
Drawn by: Frank L	Page Title: **CONTENT**
Approved: Team	Size: C

Document Number: SCH-28981 PDF: SPF-28981

Rev: A2

Date: Tuesday, January 12, 2016   Sheet 1 of 4

1. Unless Otherwise Specified:

    All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.

    All capacitors are in uF, some are 10% or 20%

    All voltages are DC

    All polarized capacitors are tantalum

2. Interrupted lines coded with the same letter or letter
   combinations are electrically connected.

3. Device type number is for reference only. The number
   varies with the manufacturer.

4. Special signal usage:
    _B Denotes - Active-Low Signal
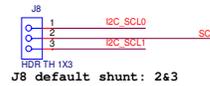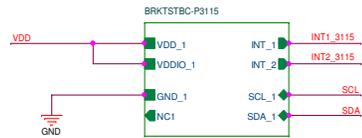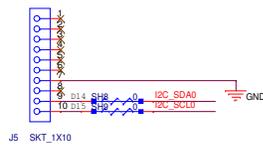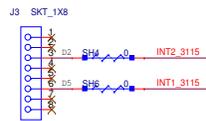   <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American
National Standards Institute specifications, current
revision, with the exception of logic block symbology.

MPL3115A PRESSURE SENSOR BLOCK

SHIELD BOARD ARDUINO HEADER

J3 SKT_1X8

D2 SH4 0 INT2_3115
D5 SH6 0 INT1_3115

J5 SKT_1X10

D14 SH8 0 I2C_SDA0
D15 SH9 0 I2C_SCL0

GND

BRKTSTBC-P3115

VDD — VDD_1   INT_1 — INT1_3115
      VDDIO_1   INT_2 — INT2_3115
      GND_1   SCL_1 — SCL
      NC1   SDA_1 — SDA

GND

SHIELD BOARD ARDUINO HEADER

J4 SKT_1X6

I2C_SCL1 SH5 0 A.5 6
I2C_SDA1 SH7 0 A.4 5

GND

VDD

C5
2.2UF

GND

J6 SKT_1X8

J7

1 I2C_SDA0
2
3 I2C_SDA1

SDA

HDR TH 1X3

J7 default shunt: 2&3

J8

1 I2C_SCL0
2
3 I2C_SCL1 SCL

HDR TH 1X3

J8 default shunt: 2&3

ICAP Classification: FCP: ___   FIUO: X   PUBI: ___
Drawing Title:
FRDMSTBC-P3115
Page Title:
NOTES

Size C	Document Number		Rev
	SCH-28981 PDF: SPF-28981		A2
Date:	Tuesday, January 12, 2016	Sheet 3 of 4	

EDGE OF BRKOUT BOARD

MPL3115A PRESSURE SENSOR

VDD
VDDIO

VDD_3115
VDDIO_3115

VDD
VDDIO

C2
0.1UF
C1
10uF

R1
4.7K
R2
4.7K

J1

VDD_1
VDDIO_1
GND_1
NC1

SH1
SH2   0

HDR 1X4
DNP

GND
GND

U1

1 VDD   SCL 8   SCL
2 CAP   SDA 7   SDA
3 GND   INT1 6   INT1
4 VDDIO   INT2 5   INT2

MPL3115A2

VDDIO

SCL1   SDA1   INT1   INT2

J2

SCL
SDA
INT1
INT2

1
2
3
4

SCL_1
SDA_1
INT_1
INT_2

HDR 1X4
DNP

SH3
0

GND1

C4
0.1UF
C3
0.1UF
DNP
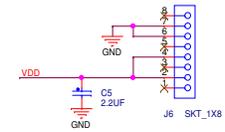
GND
GND

GND

MPL3115A

I2C ADDRESS: 0xC0

ICAP Classification: FCP: ___   FIUO: X   PUBI: ___
Drawing Title:
FRDMSTBC-P3115
Page Title:
NOTES
Size
C
Document Number
SCH-28981 PDF: SPF-28981
Rev
A2
Date: Tuesday, January 12, 2016   Sheet   4   of   4