

MCF51AG128 Reference Manual Errata

by: Microcontroller Division

This errata document describes corrections to the *MCF51AG128 Reference Manual*, order number MCF51AG128RM. For convenience, the addenda items are grouped by revision. Please check our website at <http://www.freescale.com/coldfire> for the latest updates.

The current available version of the *MCF51AG128 Reference Manual* is Revision 5.

Contents

1	Errata for Revision 5	2
2	Revision History	2

1 Errata for Revision 5

Table 1. MCF51CN128RM Rev 5 Errata

Location	Description
Table 7-3 / Page 7-8	<p>Updated CPUCR[23] to: FHP- Crossbar Force High Priority Arbitration. Elevates the processor's fixed crossbar arbitration from lowest to highest. 0 Do not force the elevation of the processor's fixed crossbar arbitration from lowest to highest. 1 Force the elevation of the processor's fixed crossbar arbitration from lowest to highest.</p> <p>Updated CPUCR[26] to: HAE- Crossbar High Priority Arbitration Enable. Elevates the processor's fixed crossbar arbitration from lowest to highest during the processing of an interrupt service routine. 0 Do not enable the processor's fixed crossbar arbitration from lowest to highest during the processing of an interrupt service routine. 1 Enable the processor's fixed crossbar arbitration from lowest to highest during the processing of an interrupt service routine.</p>
Chapter 6 / Page 6-1	<p>Corrected 'high drive' to 'low drive' in the following paragraph in Introduction as:</p> <p>After reset, the shared peripheral functions are disabled and the pins are configured as inputs (PTxDDn = 0). The pin control functions for each pin are configured as follows: slew rate control disabled (PTxSREn = 0), high drive strength selected (PTxDSn = 0), and internal pulls disabled (PTxPUEn = 0).</p>

2 Revision History

Table 2 provides a revision history for this document.

Table 2. Revision History Table

Rev. Number	Substantive Changes	Date of Release
0	Various technical and other corrections as described in Table 1 .	3/2011

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