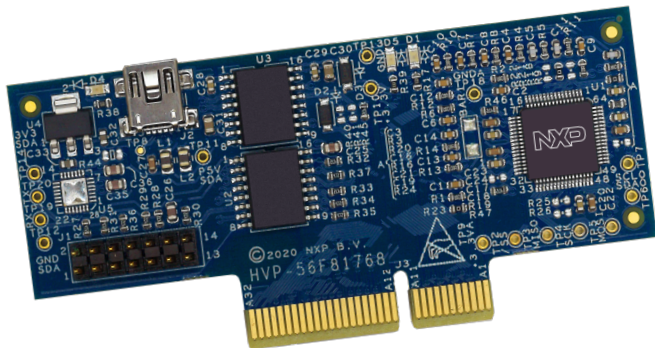




QUICK START GUIDE

HVP-56F81768

# HVP-56F81768 HIGH-VOLTAGE DEVELOPMENT PLATFORM



## GET TO KNOW THE HVP-56F81768

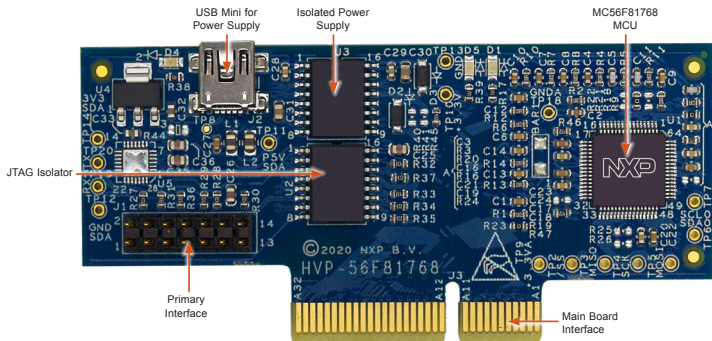


Figure 1: HVP-56F81768 Callouts

## HVP-56F81768 HIGH-VOLTAGE DEVELOPMENT PLATFORM

The NXP high-voltage development platform is a set of software and hardware tools for evaluation and development of high-voltage motor control and power conversion algorithms. NXP designed this platform for rapid prototyping of high-voltage microcontroller-based applications.

## INTRODUCTION TO HVP-56F81768 HIGH-VOLTAGE DEVELOPMENT PLATFORM

The HVP-56F81768 controller card is a development platform for the DSC 56F8xxx family, which in combination with one of HVP-MC3PH high-voltage development platform, provides ready-made software and hardware development for high-voltage motor control and power conversion applications.

### HVP-56F81768 Controller Card Features

- Accommodates target MC56F81768 MCU (32-bit DSP core with single-cycle math computation, 50/100 MHz, 128 KB flash, 2 x 12-bit ADCs, high-resolution PWM, 64 LQFP) JTAG isolation up to 5 KV
- Galvanic isolation
- Design optimized for low noise
- Onboard isolated power supply; allows safe debugging
- Controller card allows stand-alone operation

### Tools Required

- CodeWarrior® Development Studio for MCU version 11.1 or later



## GET STARTED

Download installation software and documentation under “Jump Start Your Design” at [www.nxp.com/HVP-56F81768](http://www.nxp.com/HVP-56F81768).

## SUPPORT

Visit [www.nxp.com/support](http://www.nxp.com/support) for a list of phone numbers within your region.

## WARRANTY

Visit [www.nxp.com/warranty](http://www.nxp.com/warranty) for complete warranty information.

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Document Number: HVP56F81768QSG REV 0

Agile Number: 926-47103 Rev A